


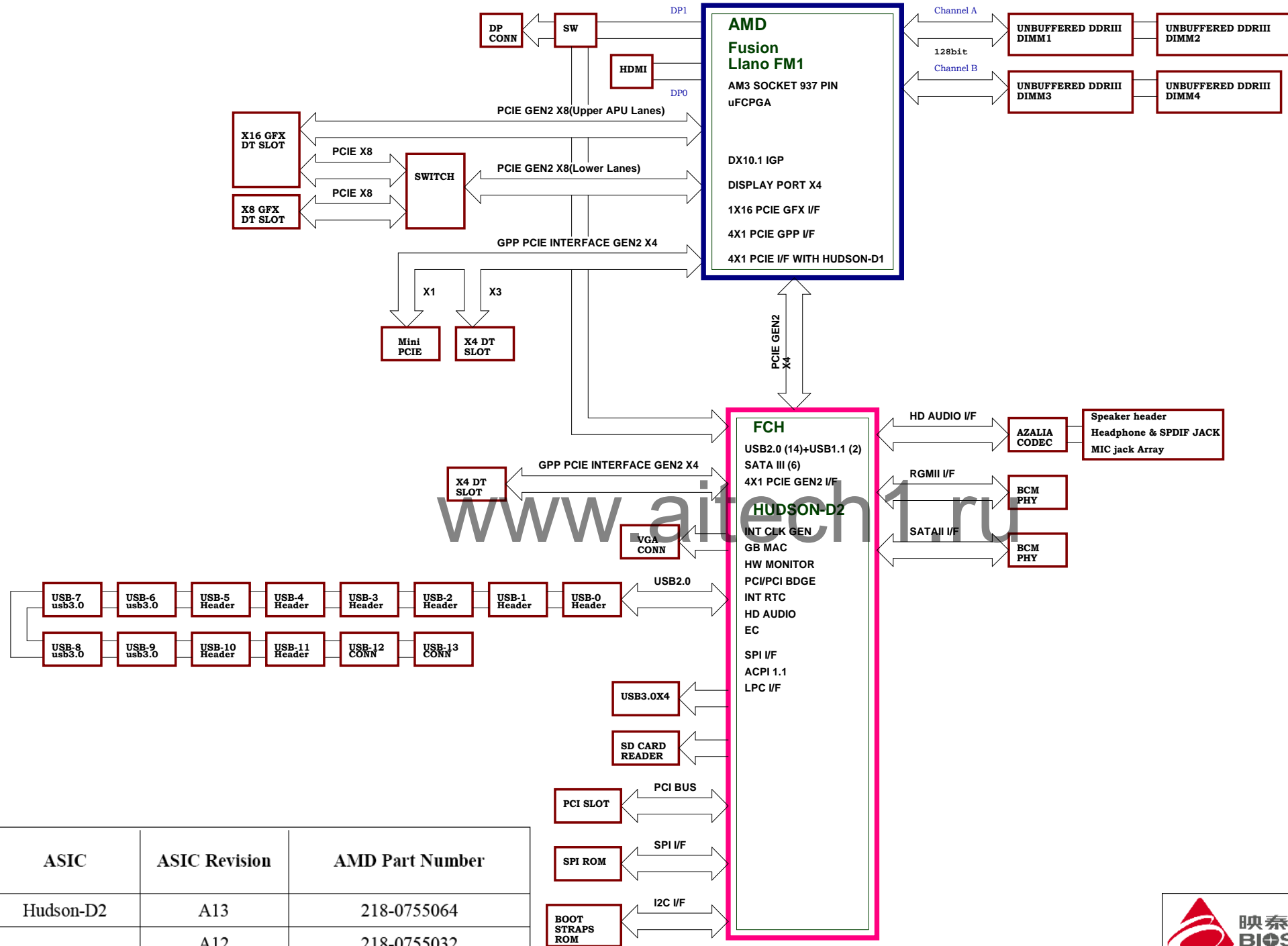
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4	CLOCK DISTRIBUTION
5	REVISION HISTROY
6-10	937 FM1 CPU
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37	ATX PWR / FRONT PANEL / LED
38	OVER VOLTAGE IC 7 NC 5
39	FRONT USB
40	PWRGD / MISC DC-DC
41	VCC_CORE DC-DC CONVER
42	MEMORY POWER
43	NB/SB CORE POWER
44	Realtek RTL8111E
45	BOM

AA55E-A1S REV 6.0

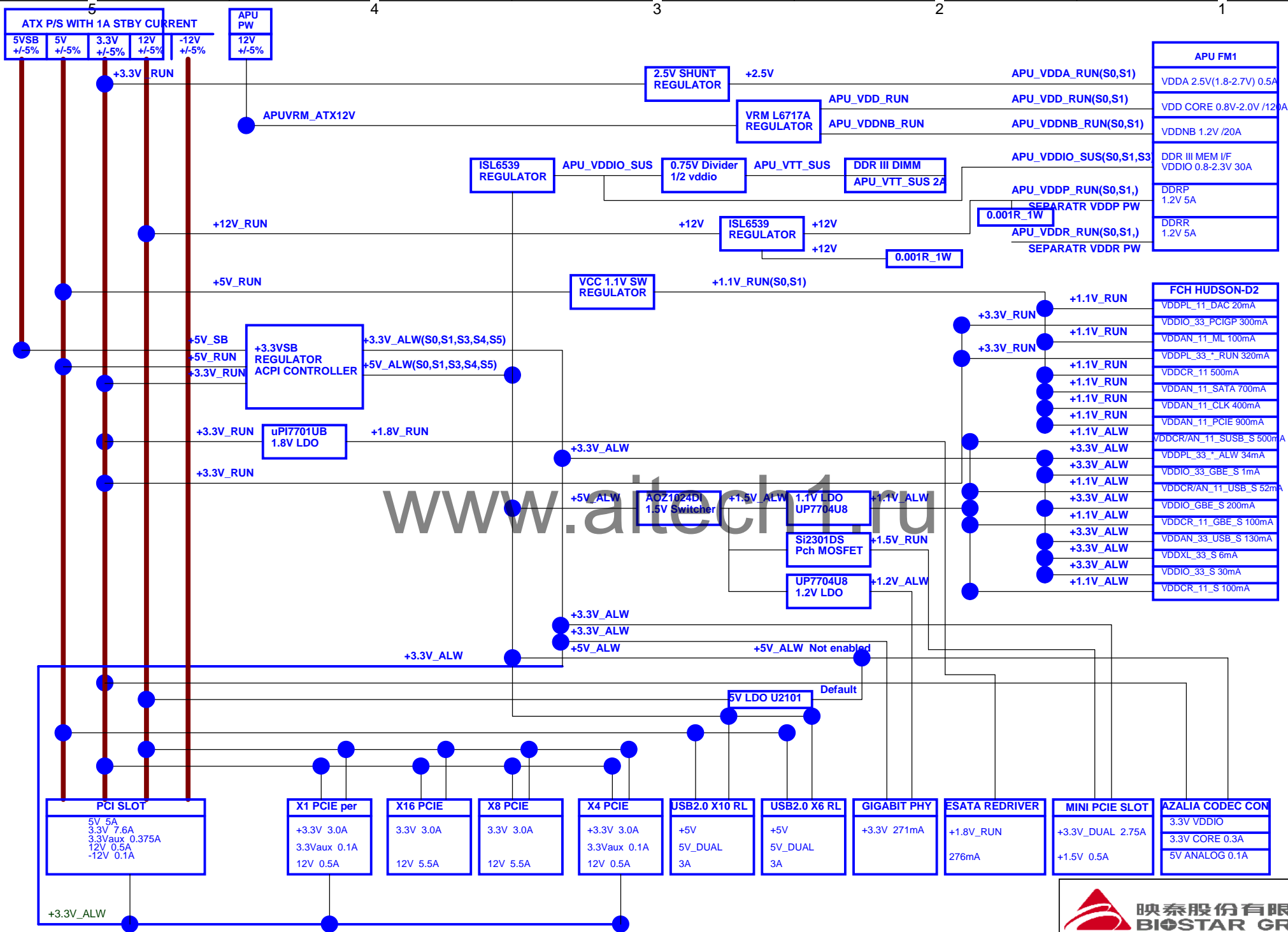
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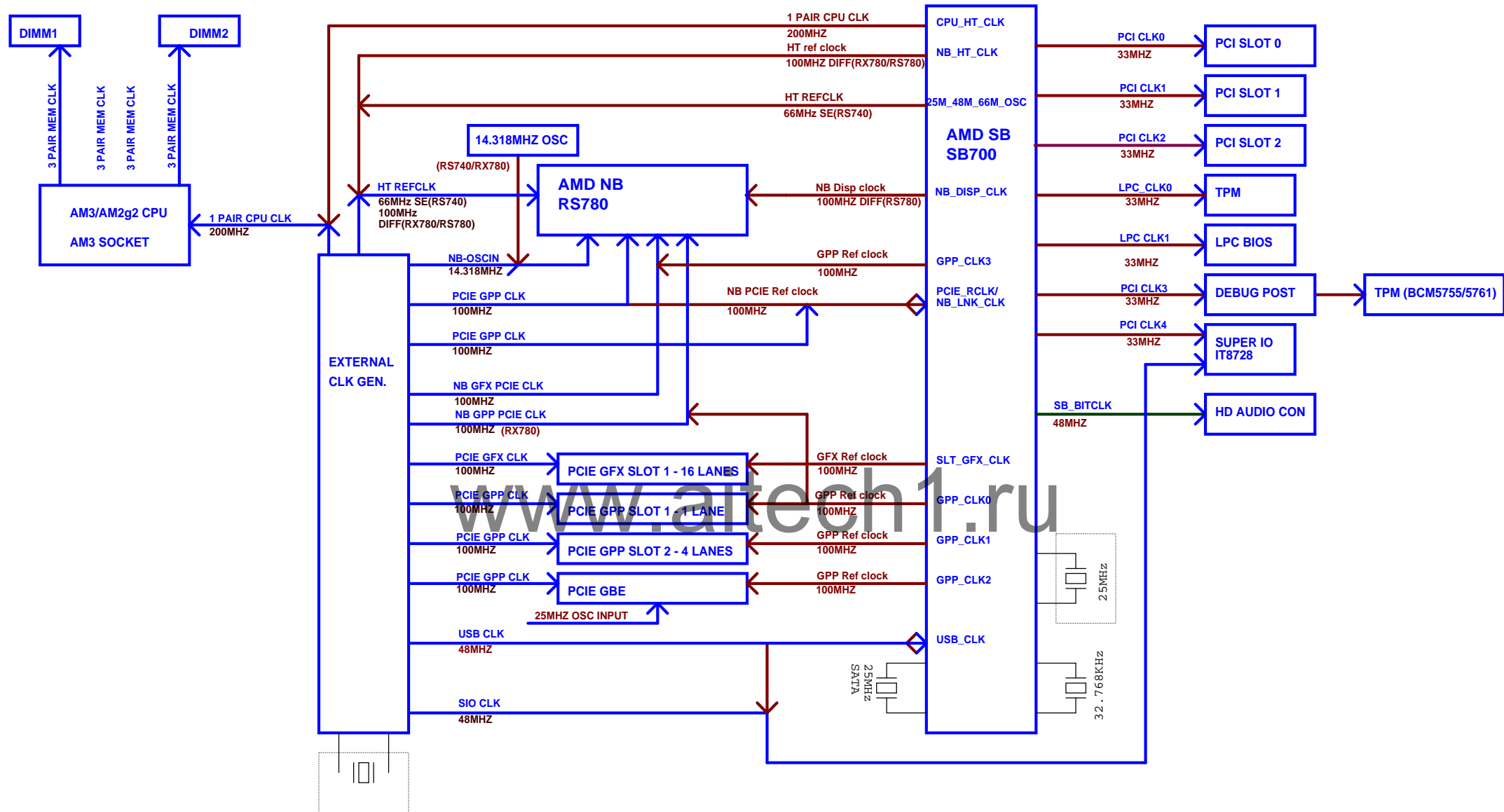
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HW Engineer:			Date:	
HW Leader1:			Date:	
HW Leader2:			Date:	
<div>◇BIOSTAR'S PROPRIETARY INFORMATION◇ ◇Any unauthorized use, reproduction, duplication, or disclosure of this document will be subject to the applicable civil and/or criminal penalties.◇</div>				
		映泰股份有限公司 BIOSSTAR GROUP		
		COVER		
		Size Custom	Document Number AA55E-A1S	Rev 6.0
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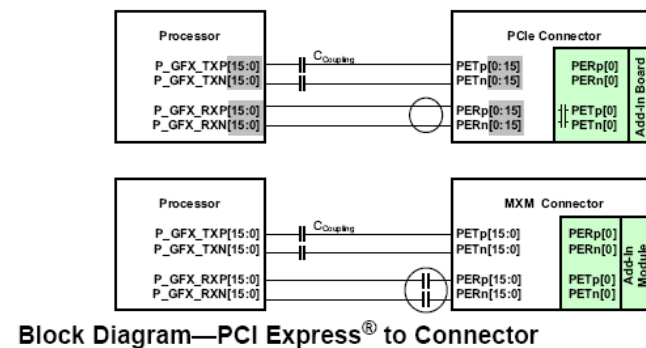
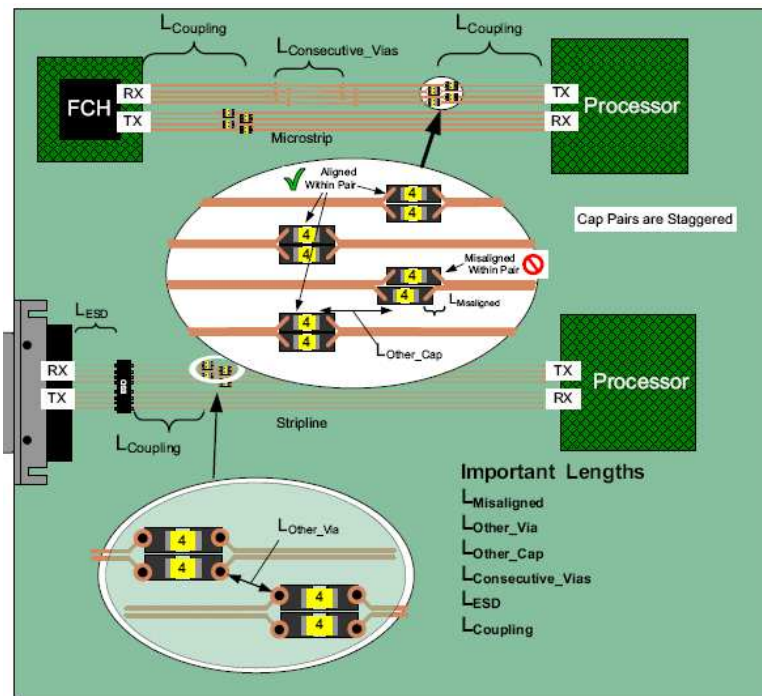
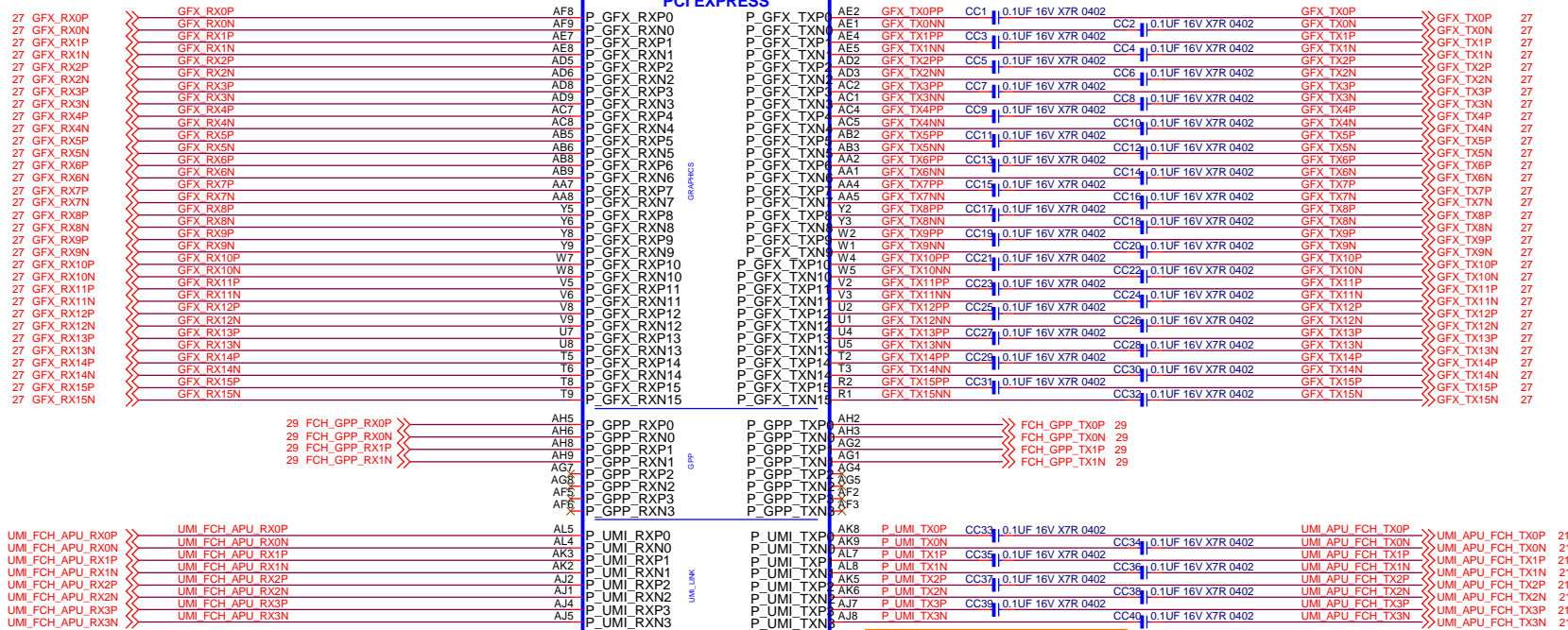
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Hudson-D3	A12	218-0755032
	A13	218-0755044

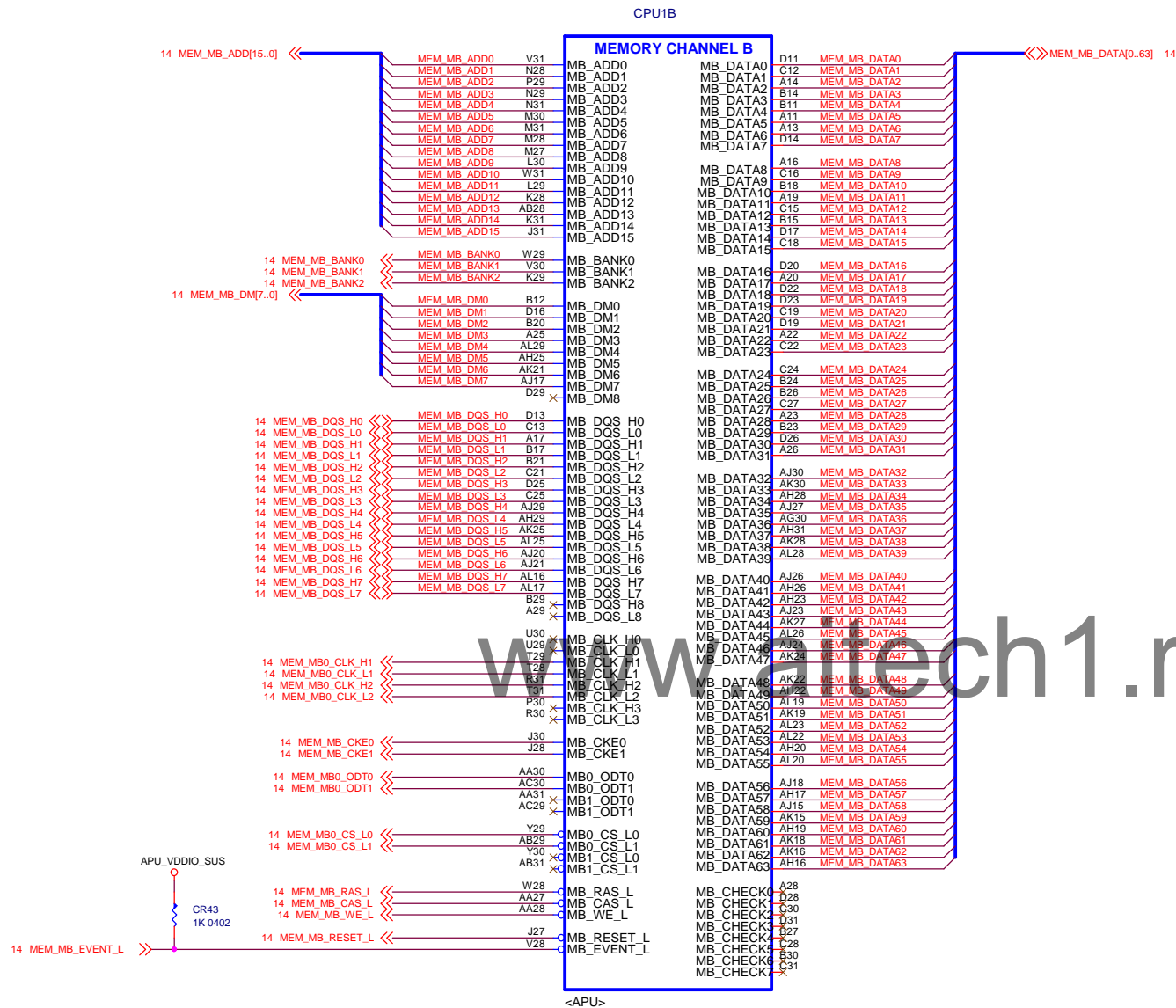


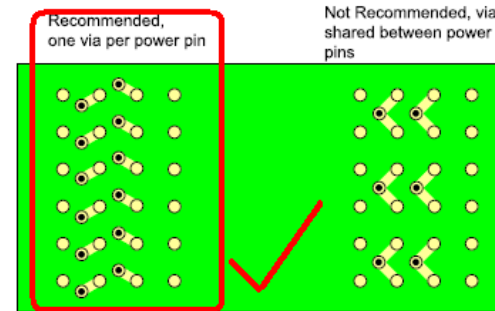
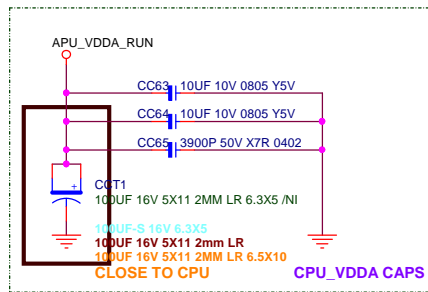
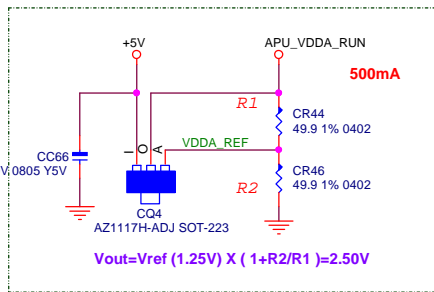
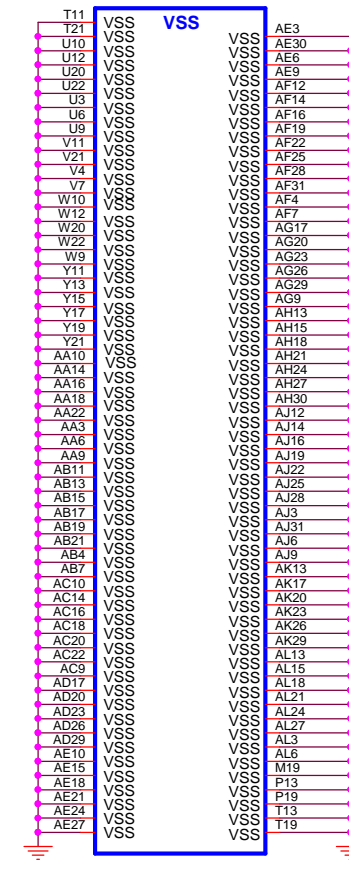
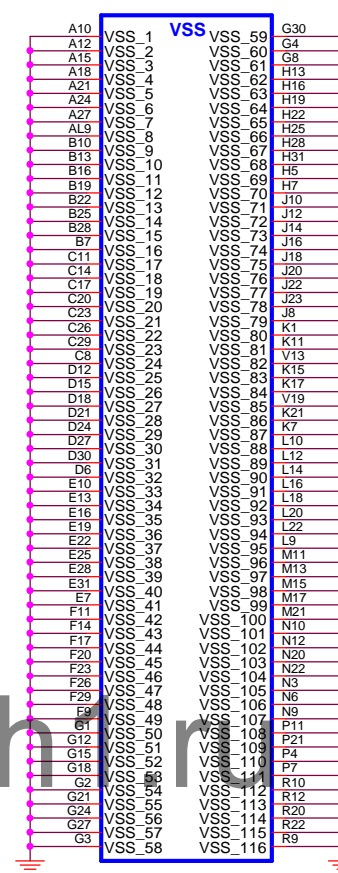
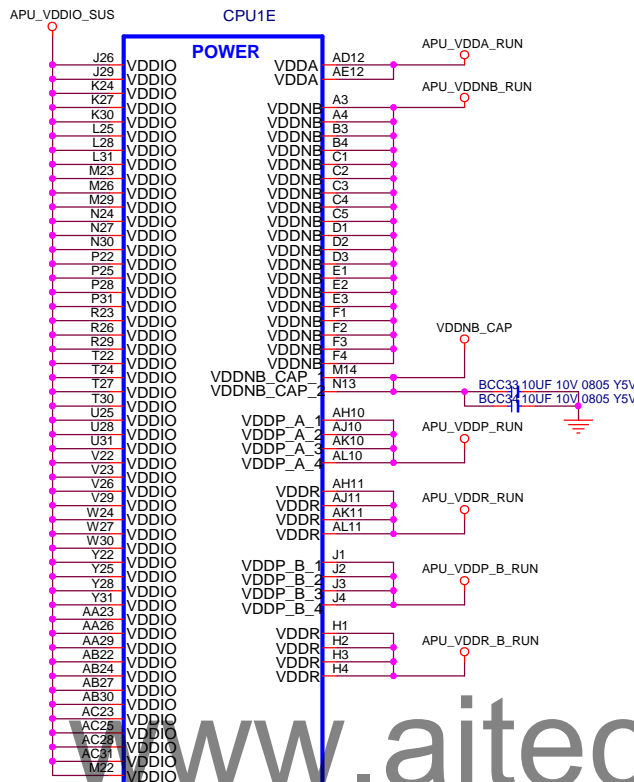
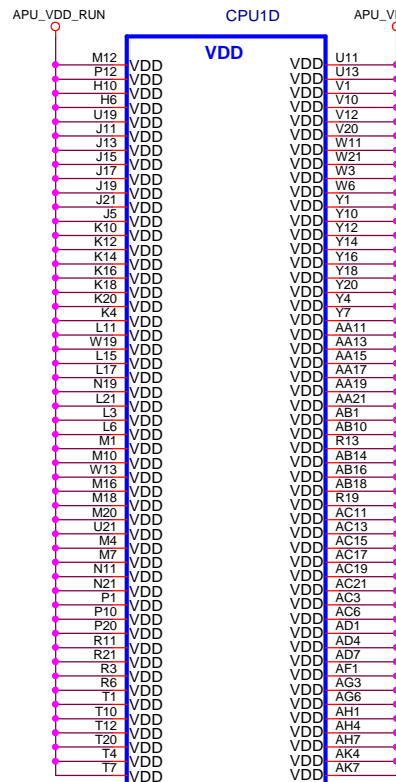


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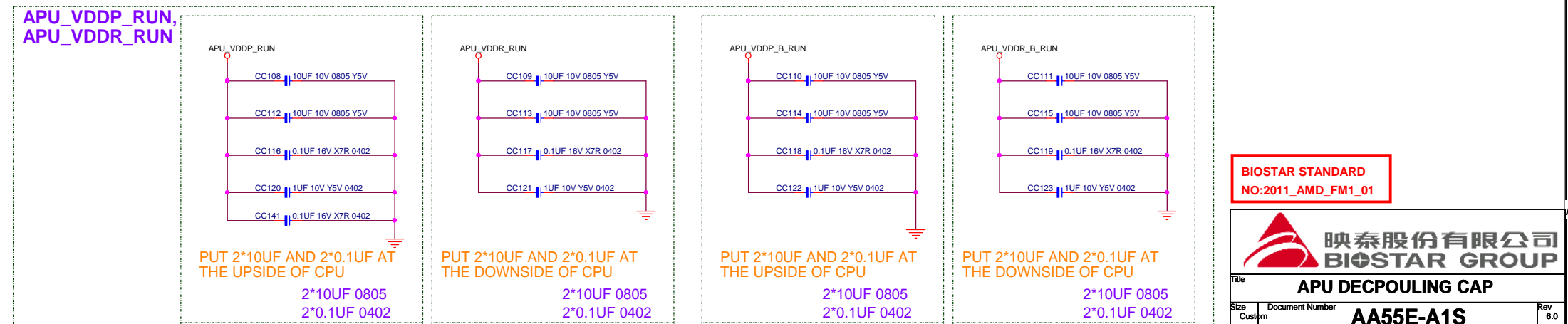
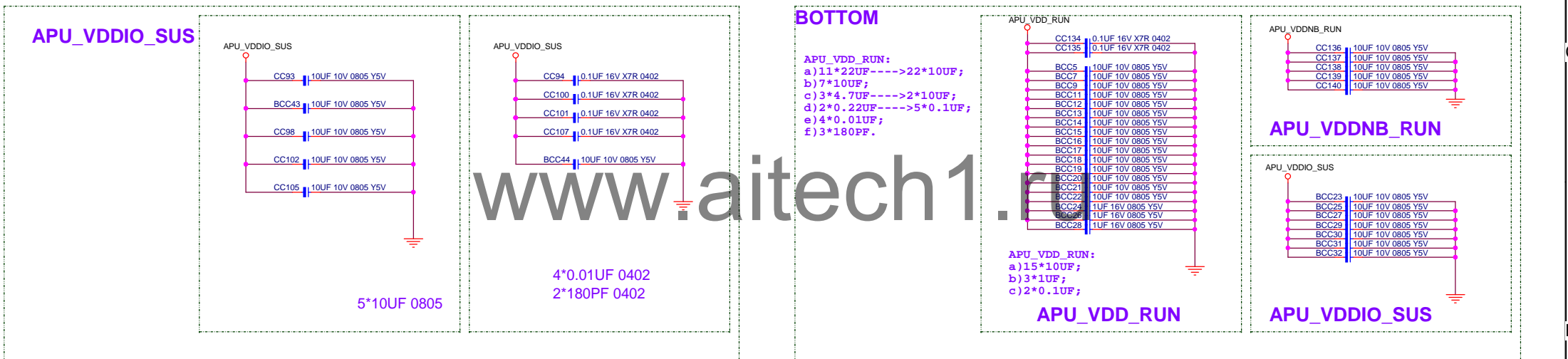
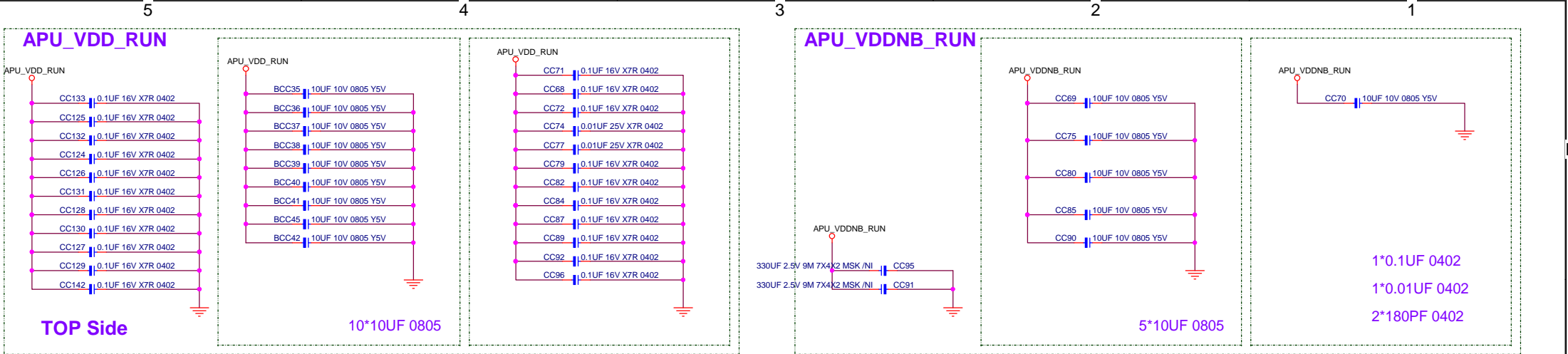
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Title			REVISION HISTORY	
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FM1										
	VSS	VDD	VDDNB	VDDIO	VDDP	VDDR to VSS		VDDA	Mvref	
Total Power Pins	416	226	102	19	51	8	8	4	4	2
Value/Size/Material										
22uF/1206/X5R		11	2	4	-	1	-	-	-	-
10uF/0805/X5R		7	2	1	2 + 1(B)	1	-	-	-	-
4.7uF/0805/X5R		3	1	4	2	2 + 2	2	2	1	-
0.22uF/0603/X5R		2	2	2 + 2	2	2 + 2	2	2	1	-
0.1uF/0603/X5R		-	-	-	-	-	-	-	-	1
0.01uF/0603/X5R		4	-	-	-	-	-	-	-	-
3.3nF/0603/X5R		-	-	-	-	-	-	-	1	-
1nF/0603/X5R		-	-	-	4	-	-	-	-	1
180pF/0402/C0G		3	1	2 + -	4	2	-	-	-	-

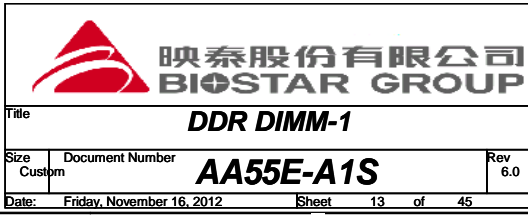


BIOSTAR STANDARD
NO:2011_AMD_FM1_01

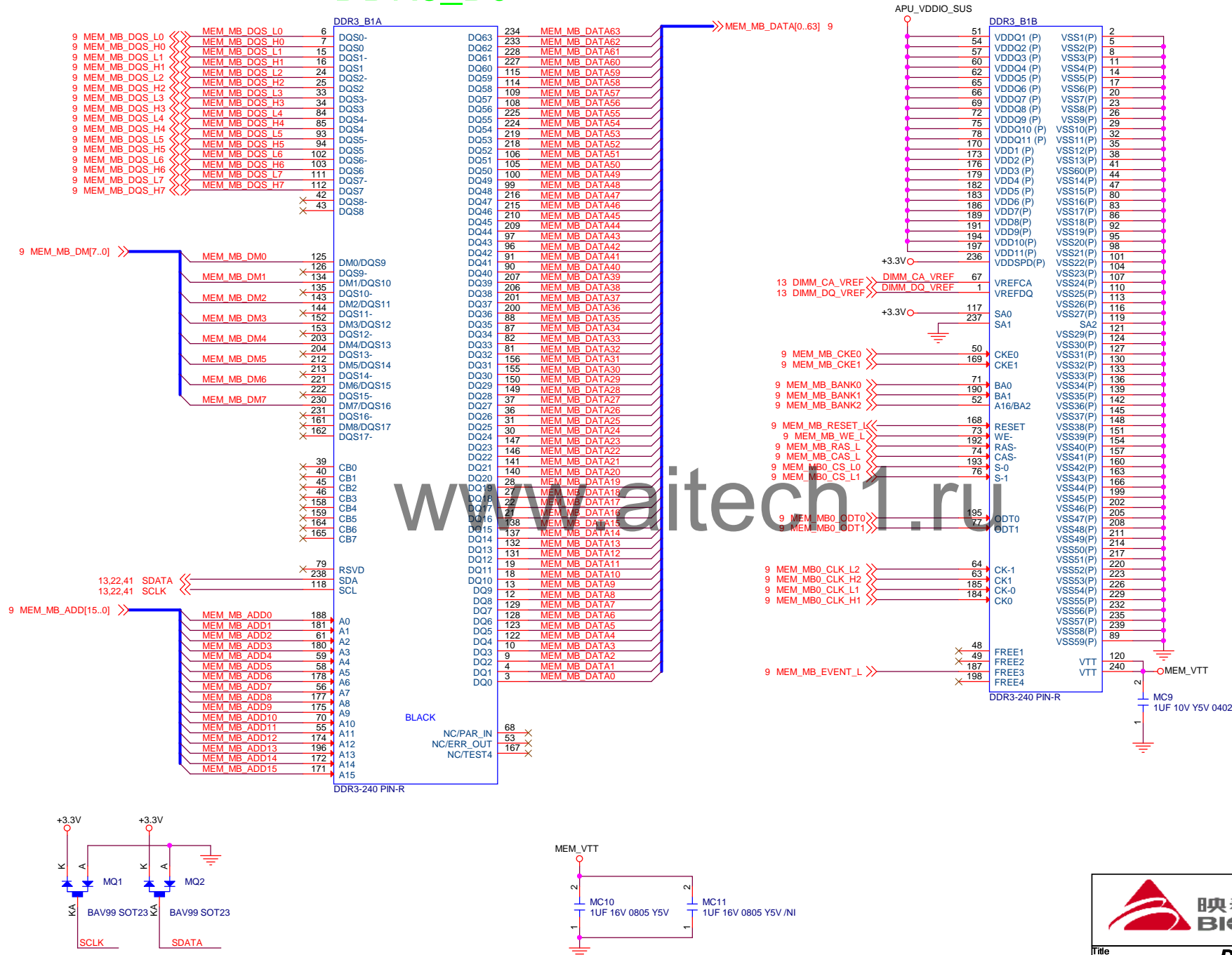
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			映泰股份有限公司 BIOSTAR GROUP		
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DDR3_A1A




DDR3_B0



DDR3_A1

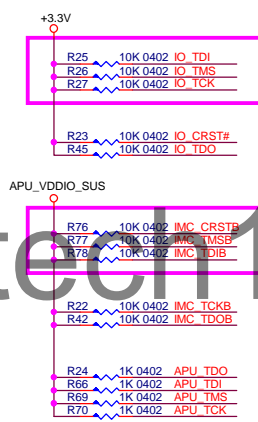
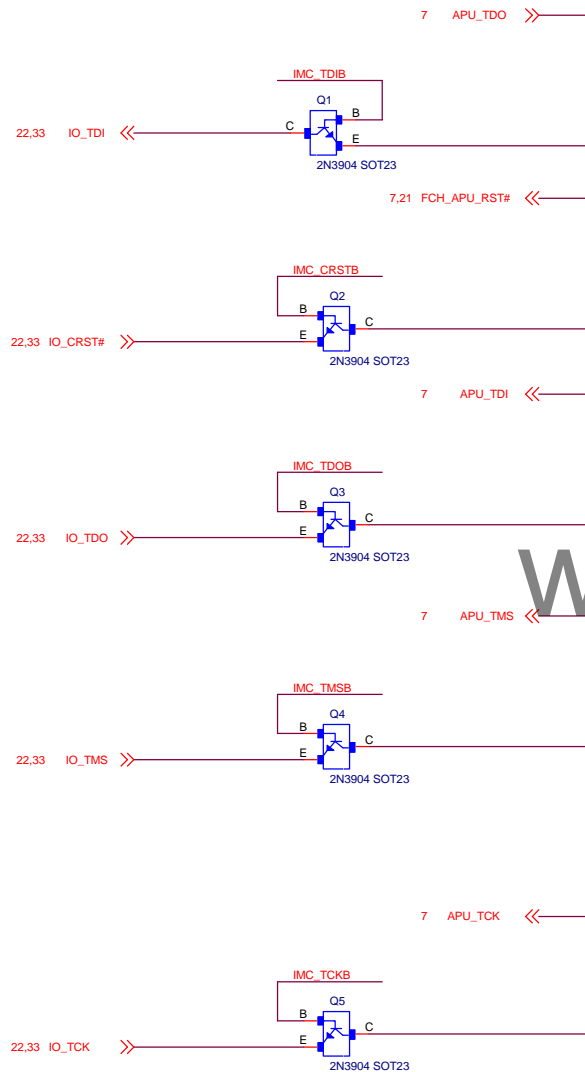
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DDR3_B1

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Title		
SB HW MONITOR		
Size	Document Number	Rev
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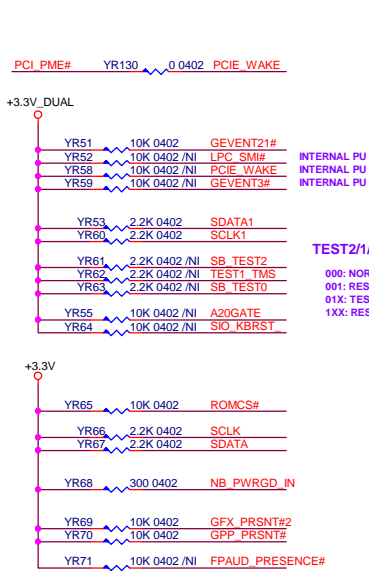
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BIOSTAR STANDARD
NO:2010_CLK_793_64P_01



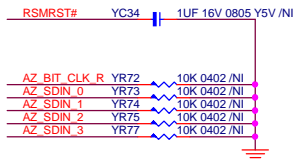
映泰股份有限公司
BIOSTAR GROUP

Title			CLOCK GEN		
Size	Document Number				Rev
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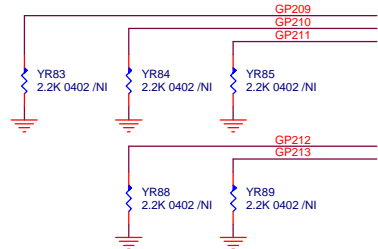


TEST2/I/O STRAP DESCRIPTION

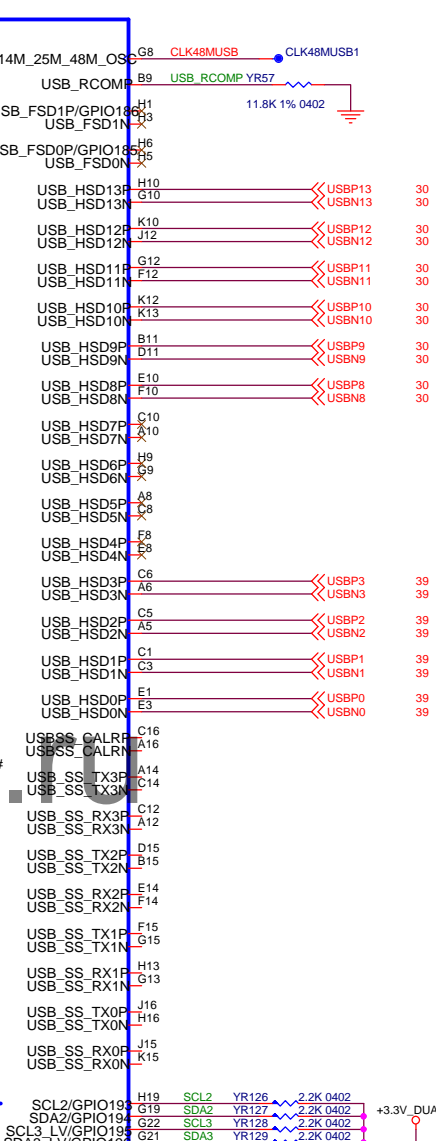
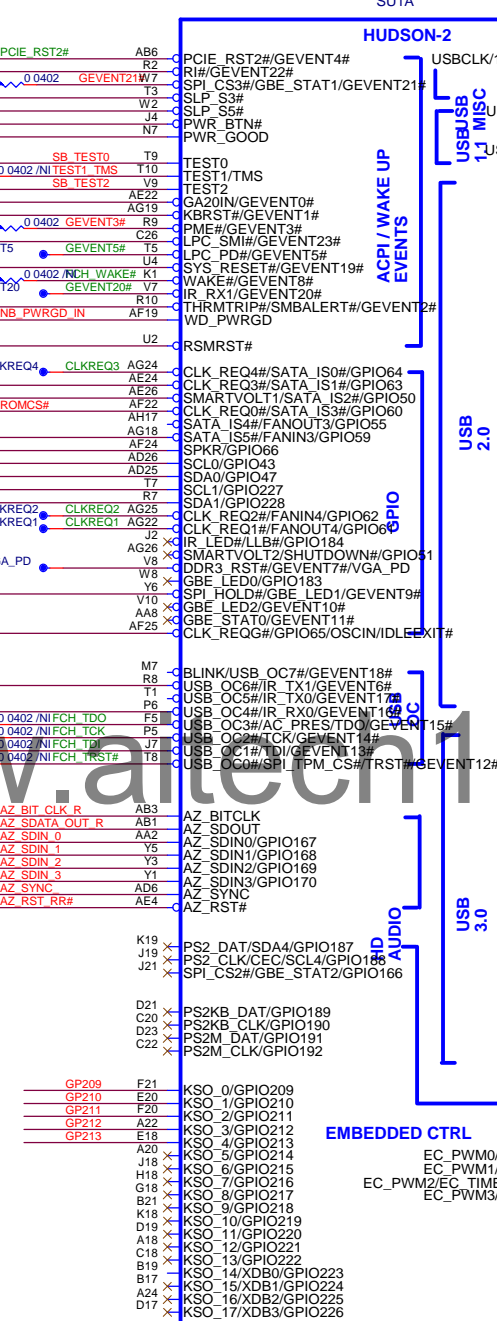
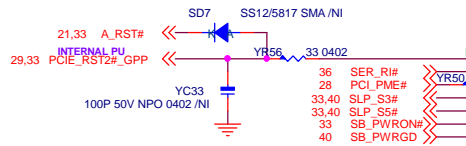
000: NORAMAL OPERATION
001: RESERVED FOR ASCII DEBUG
01X: TEST MODE
1XX: RESERVED FOR ASCII DEBUG



BOARD ID INTERNAL PULL UP



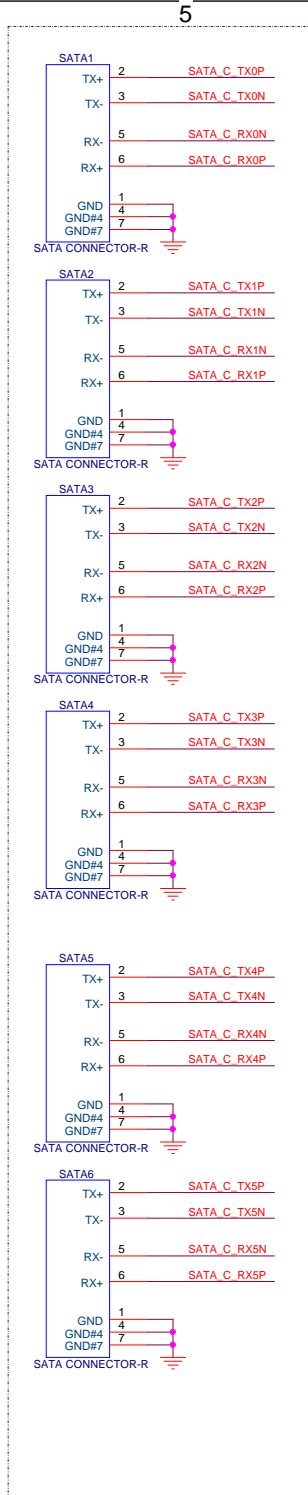
PCIE_RST2# FOR FCH PCI DEVICE



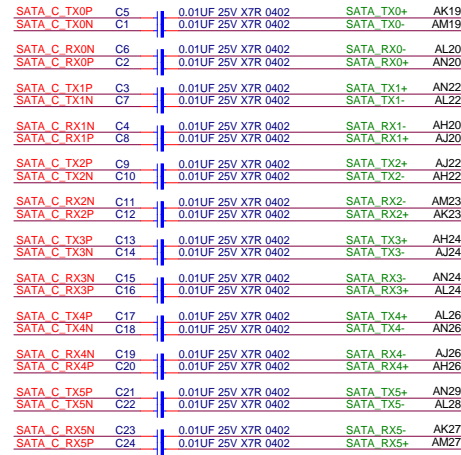
CALRP/CALRN close to FCH, 1" max

8 inches max

A55 FCH



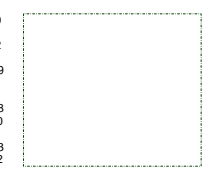
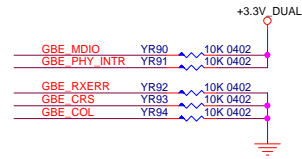
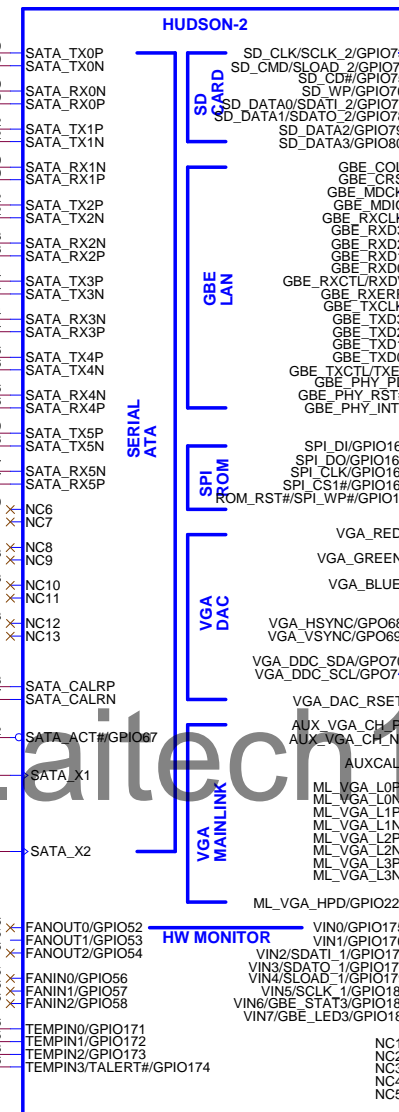
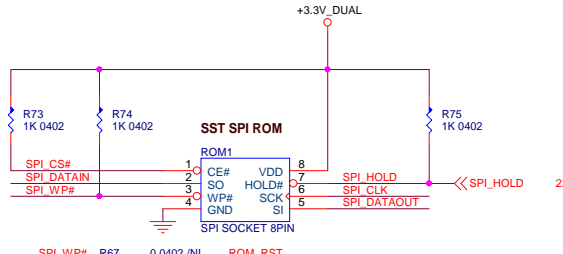
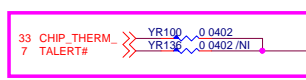
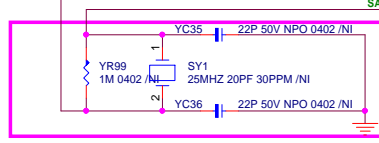
SATA RX/TX: 90ohm (20)15-4.5-7.5-4.5-15(20)

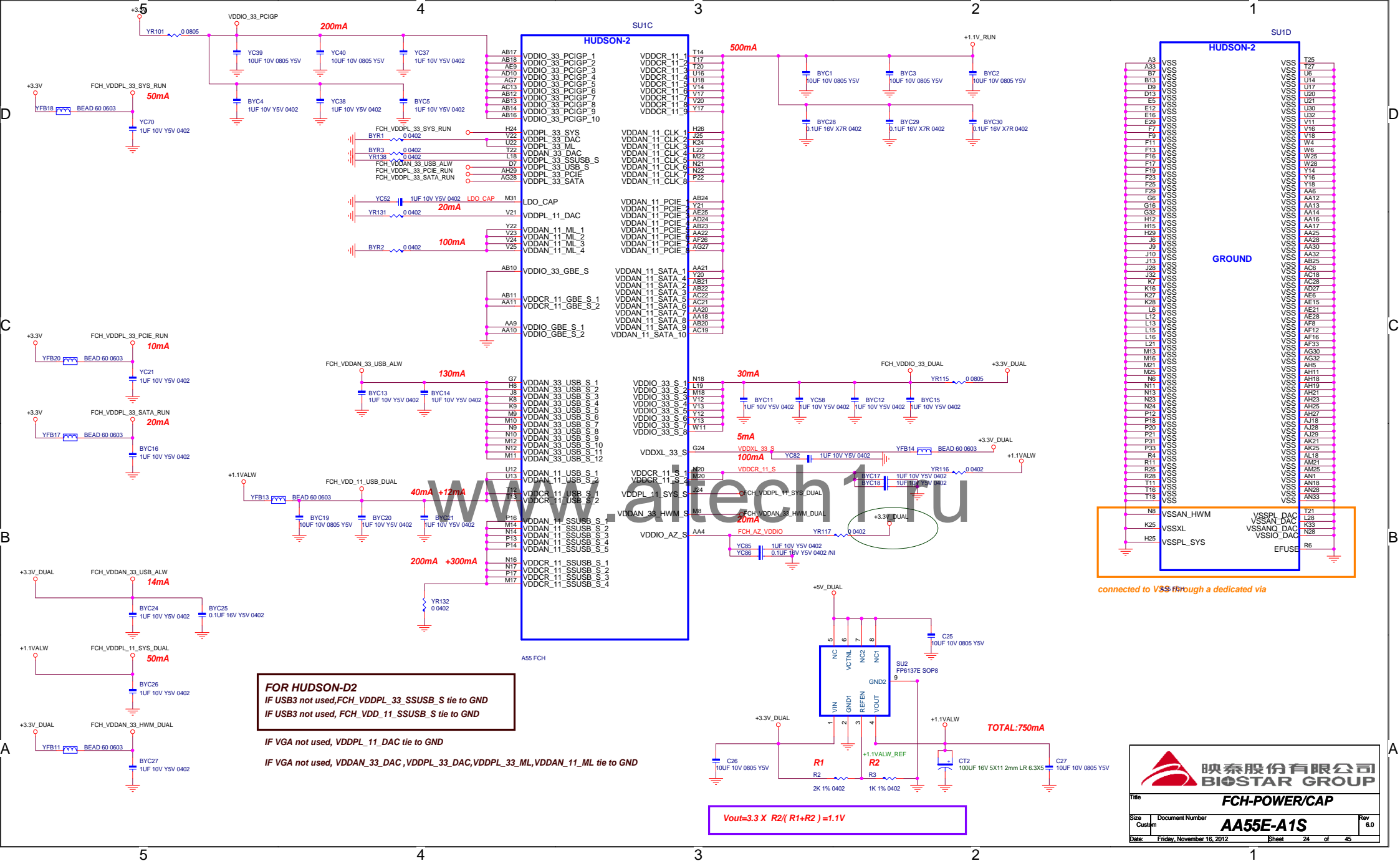


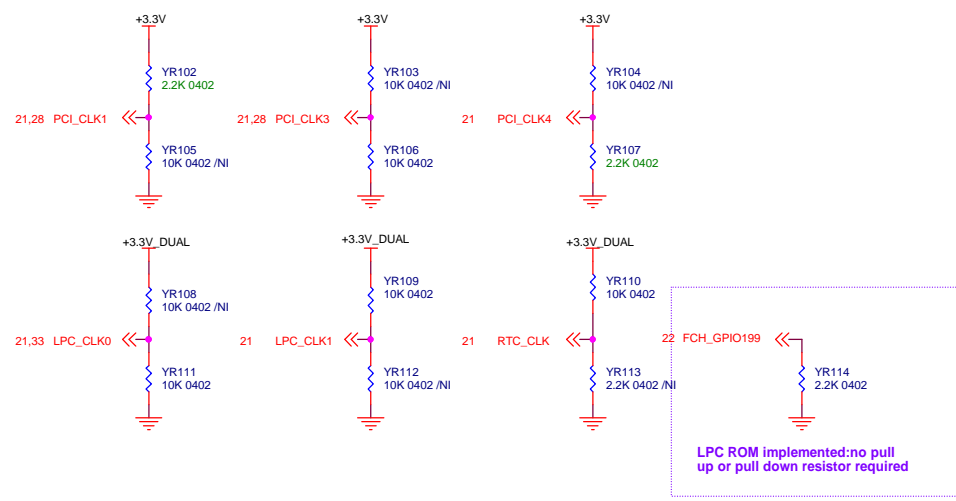
Actual location of capacitors along the pair is not critical



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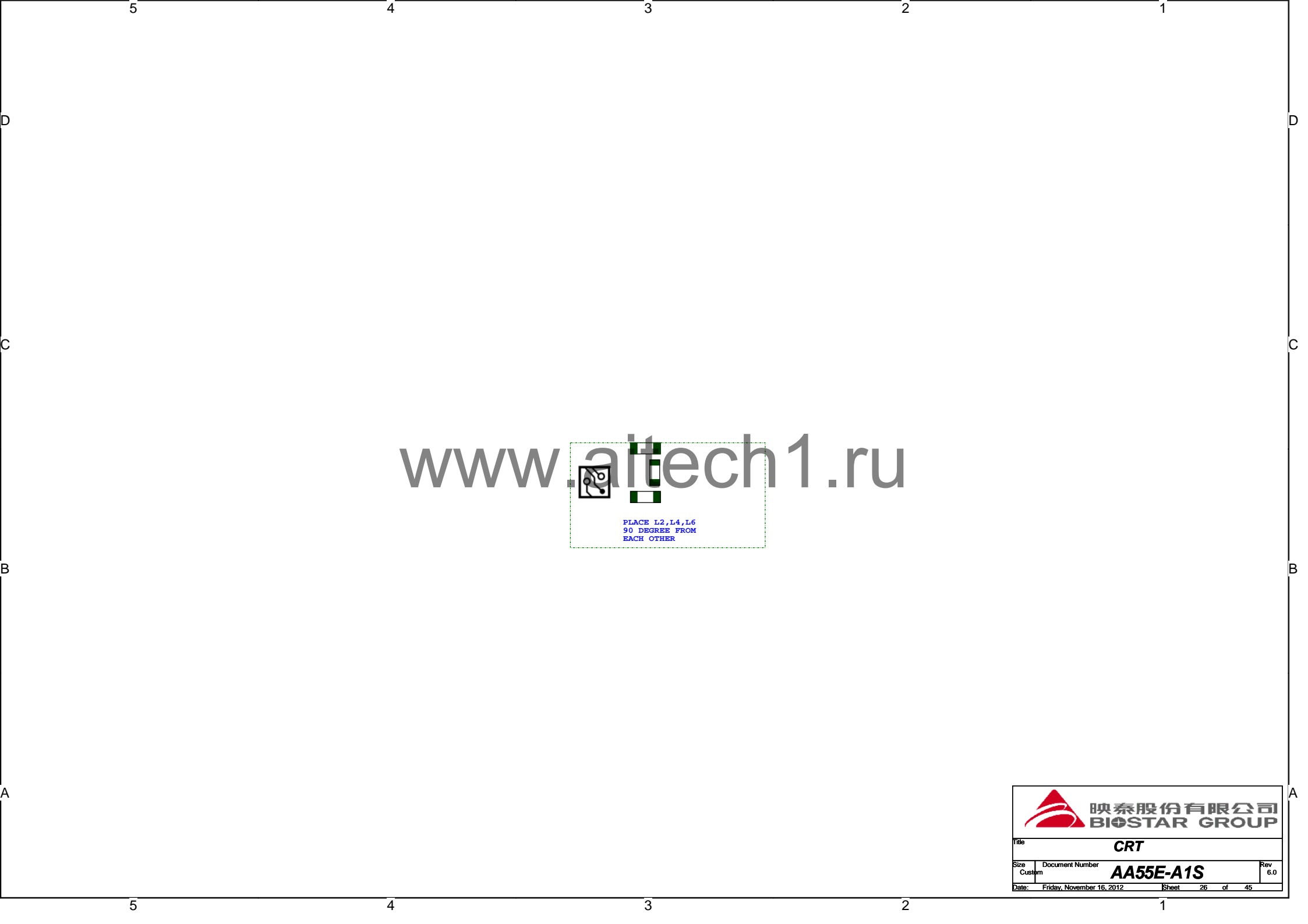




REQUIRED STRAPS

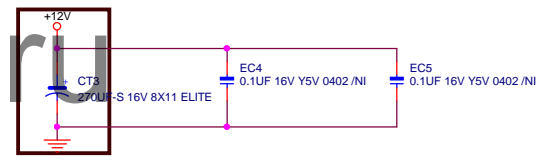
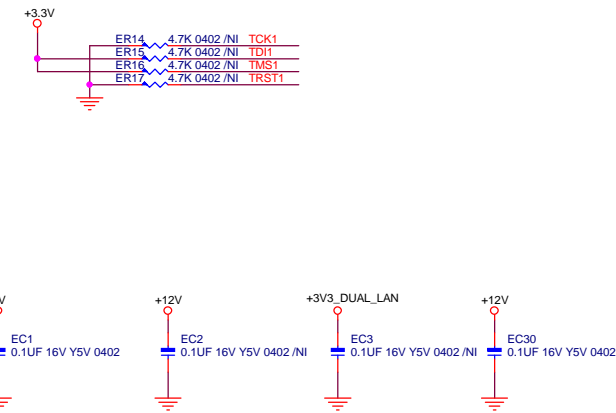
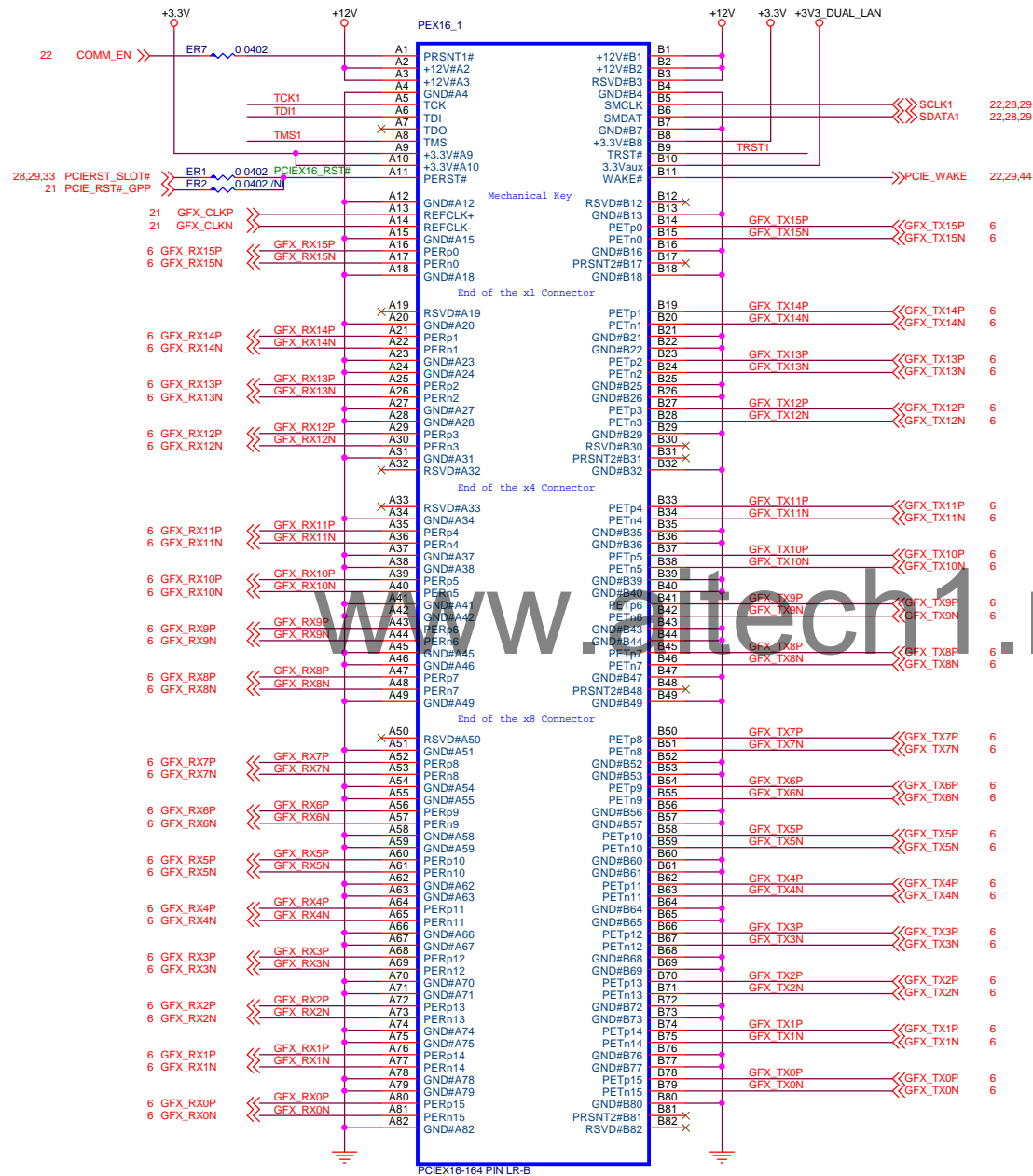
	RTC_CLK	PCI_CLK1	PCI_CLK3	PCI_CLK4	LPC_CLK0	LPC_CLK1	GPIO199
PULL HIGH	S5+ DISABLE DEFAULT	ALLOW PCIE GEN2 DEFAULT	USE DEBUG STRAPS	NON-FUSION CPU CLOCK MODE	EC ENABLED	CLKGEN ENABLED DEFAULT	ROM TYPE: H=LPC ROM
PULL LOW	S5+ ENABLE	FORCE PCIE GEN1	IGNORE DEBUG STRAPS DEFAULT	FUSION CPU CLOCK MODE DEFAULT	EC DISABLED DEFAULT	CLKGEN DISABLED	L = SPI ROM DEFAULT

NOTE: HUDSON-D1 HAS INTERNAL 15K PULL UP RESISTOR FOR PCI_AD[30:23]

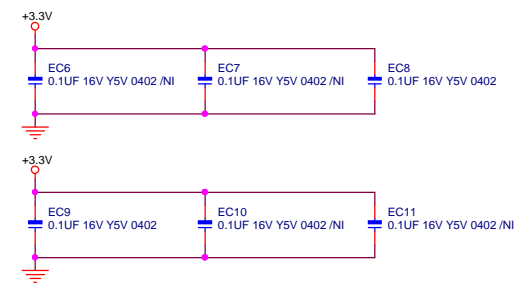


PLACE L2,L4,L6
90 DEGREE FROM
EACH OTHER

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Title CRT		
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For T-Series----270UF-S 16V 8X11 ELITE
For NO T-Series----470UF 16V 8X11.5

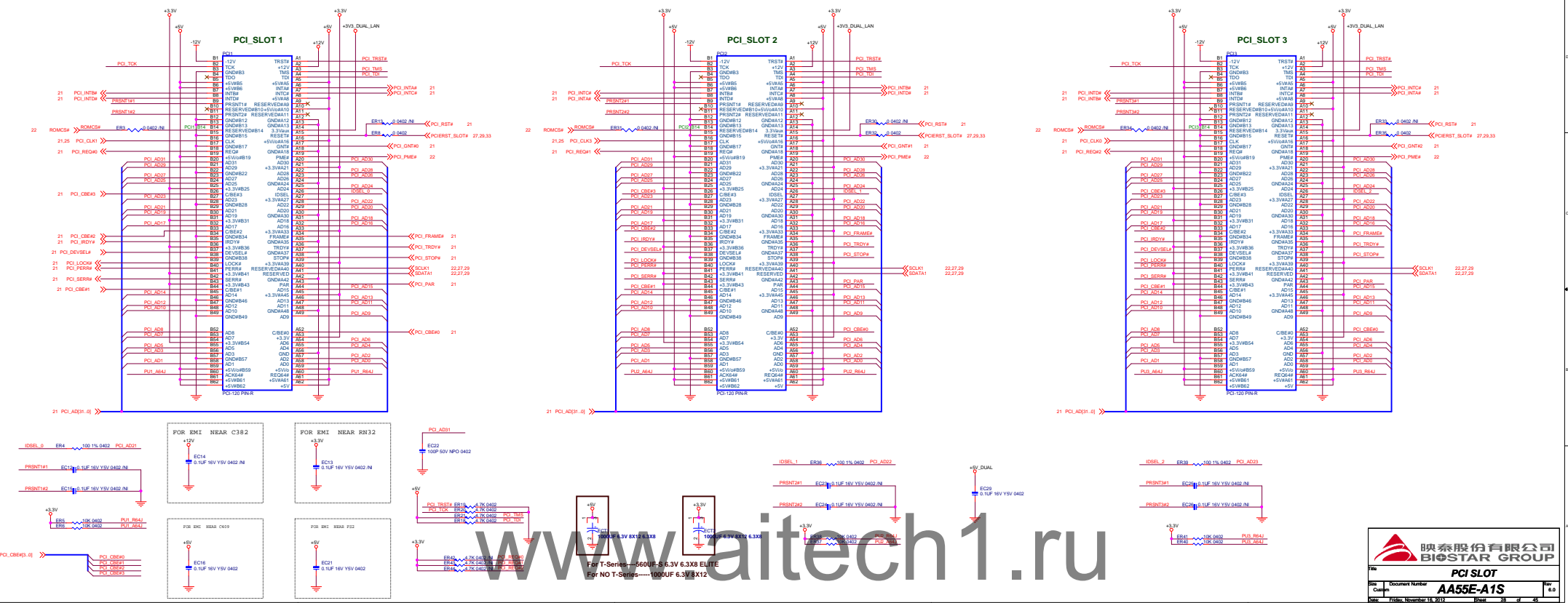


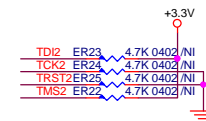
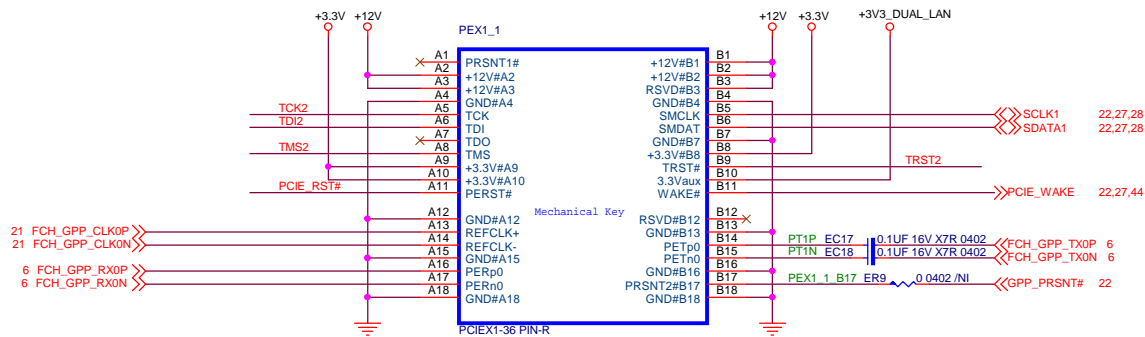
 映泰股份有限公司 BIOSTAR GROUP		
Title PEX16 SLOT		
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ISSEL:AD21, INT:ABCD, REQ0 & GNT0, PCI_CLK1

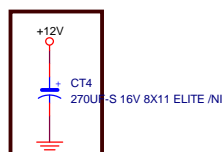
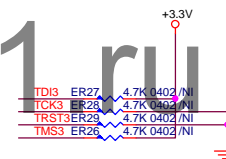
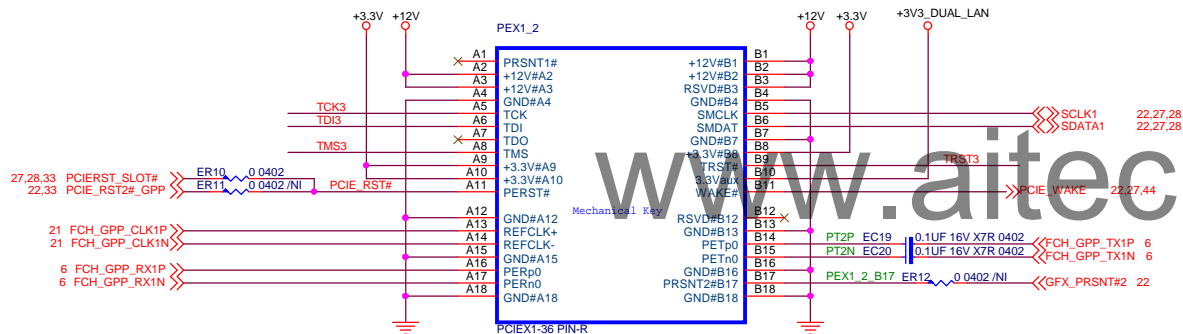
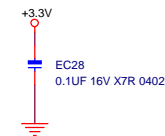
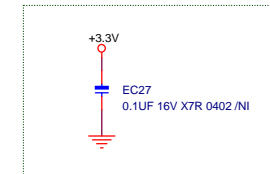
ISSEL:AD22, INT:BCDA, REQ1& GNT1, PCI_CLK3

ISSEL:AD23, INT:CDAB, REQ2& GNT2, PCI_CLK0



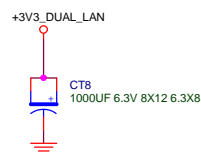


for EMI



For T-Series-----270UF-S 16V 8X11 ELITE

For NO T-Series-----470UF 16V 8X11.5



For T-Series-----560UF-S 6.3V 6.3X8 ELITE

For NO T-Series-----1000UF 6.3V 8X12

Title PEX1 SLOT * 2			
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Title

USB CONN

Size

Document Number

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Re

Date	
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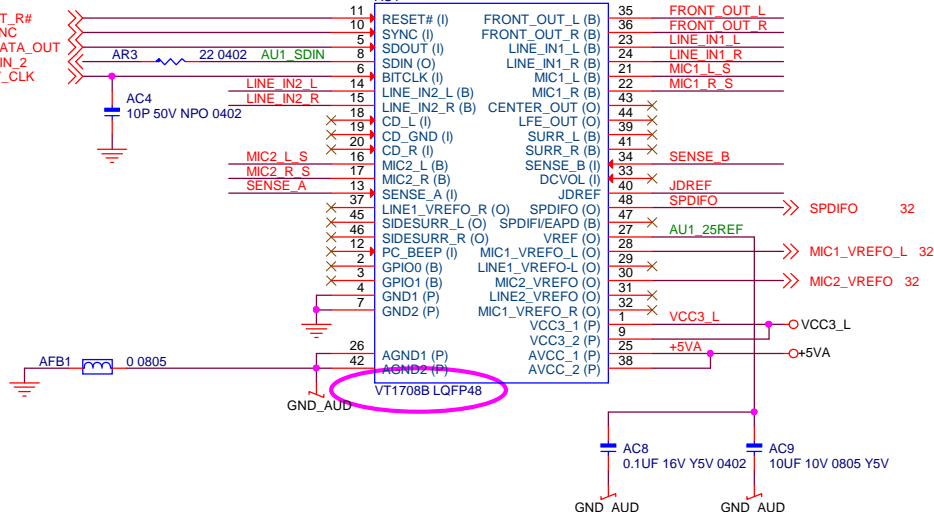
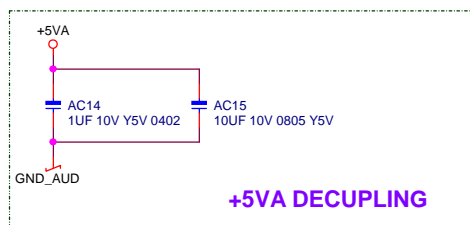
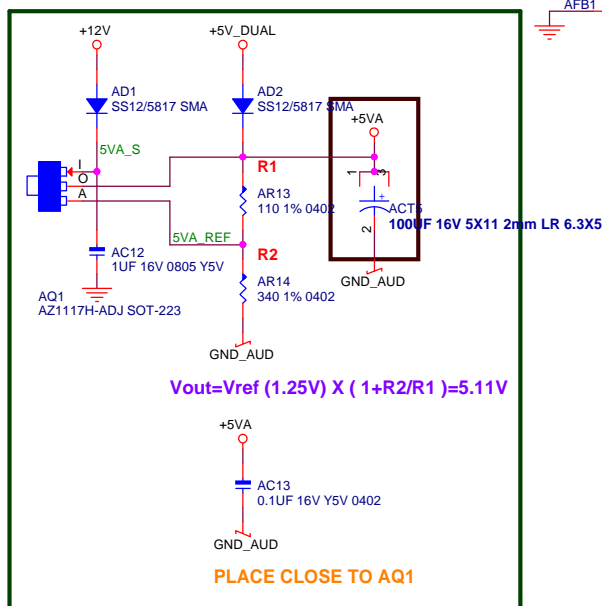
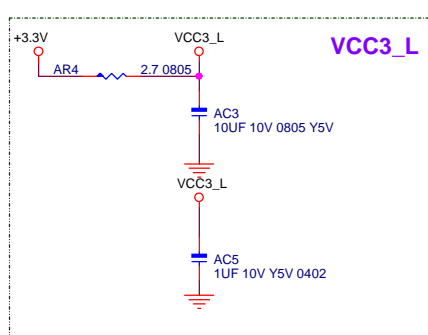
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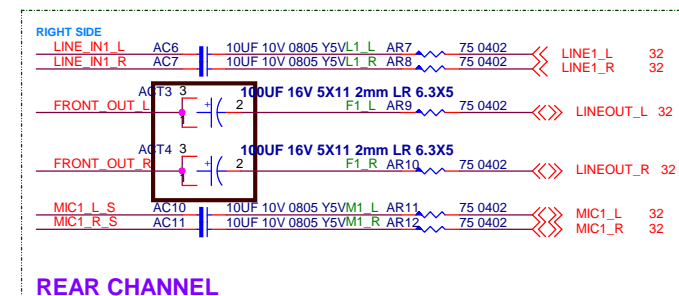
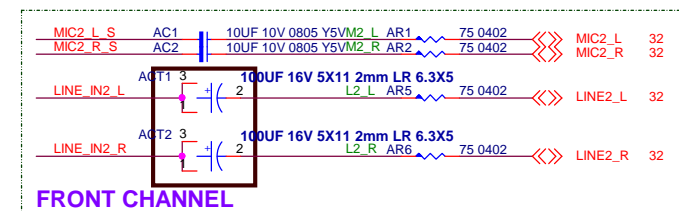
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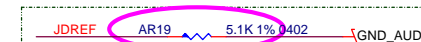
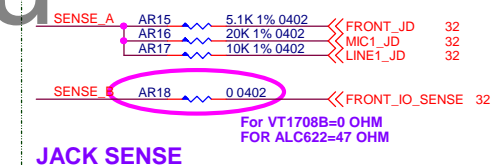
AUDIO PART: A+Reference



NETIN: 100UF 16V 5X11 2MM LR 6.3X5
BOM: 100UF 16V 5X11 2mm LR
100UF-S 16V 6.3X8 ELITE



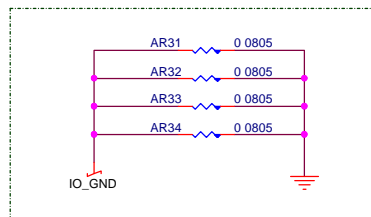
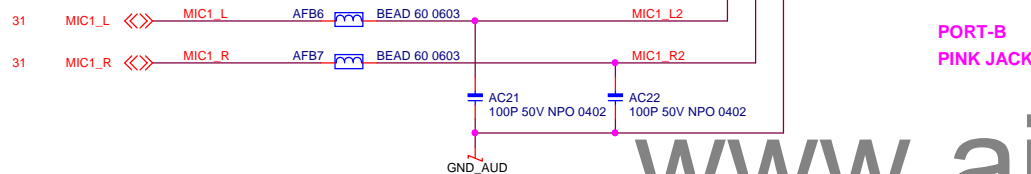
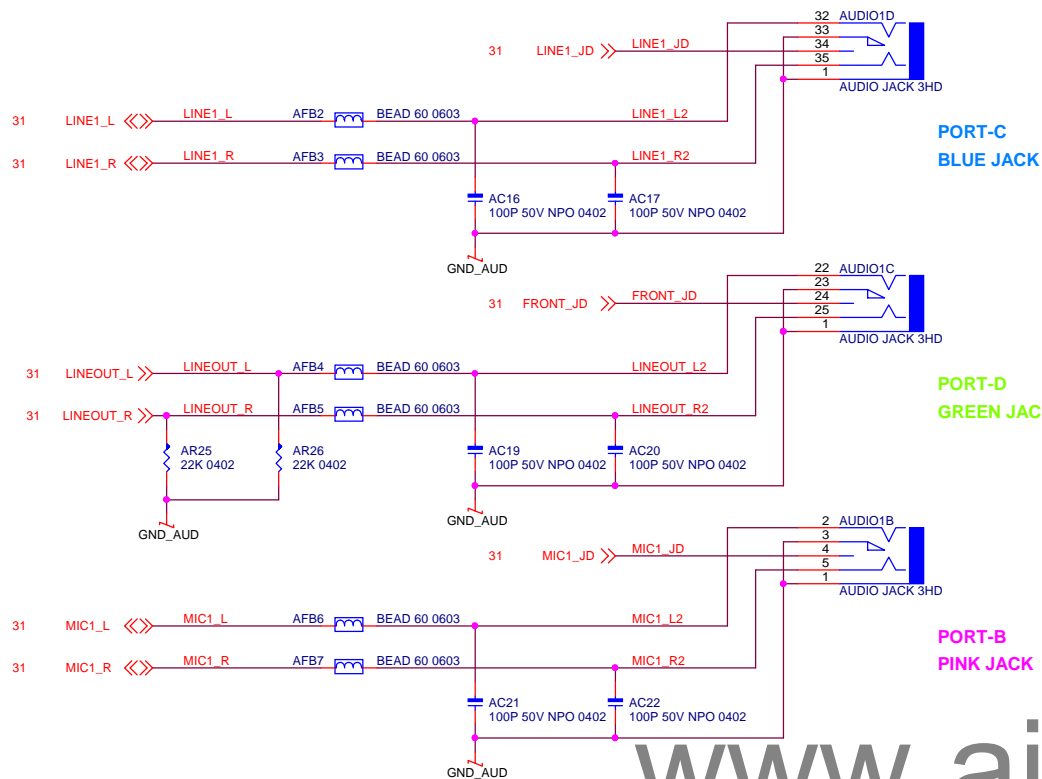
CD IN CONNECTOR



FOR VT1708B LQFP48: AR30=5.1K
FOR ALC662 LQFP48: AR30=20K

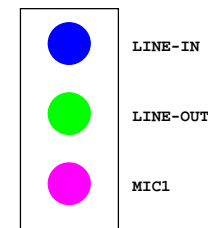
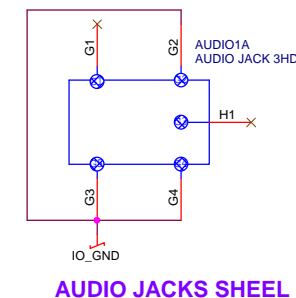
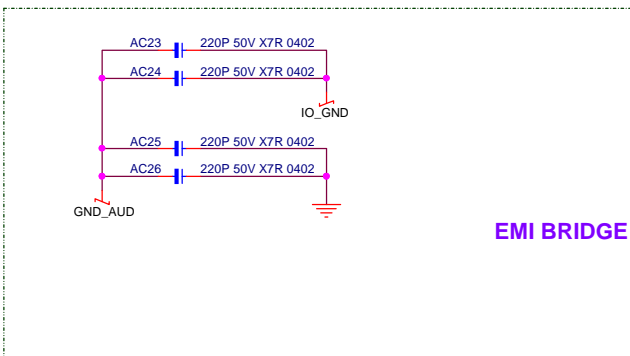
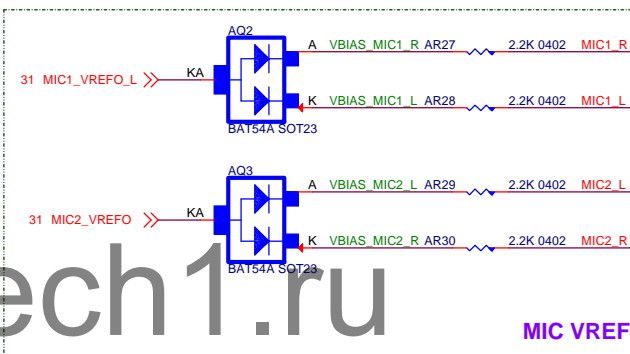
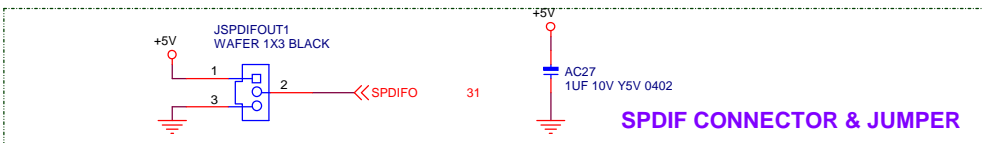
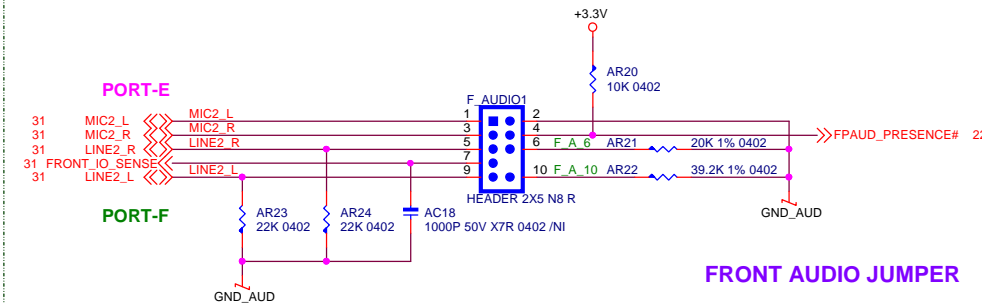


Title			
CODEC ALC 662			
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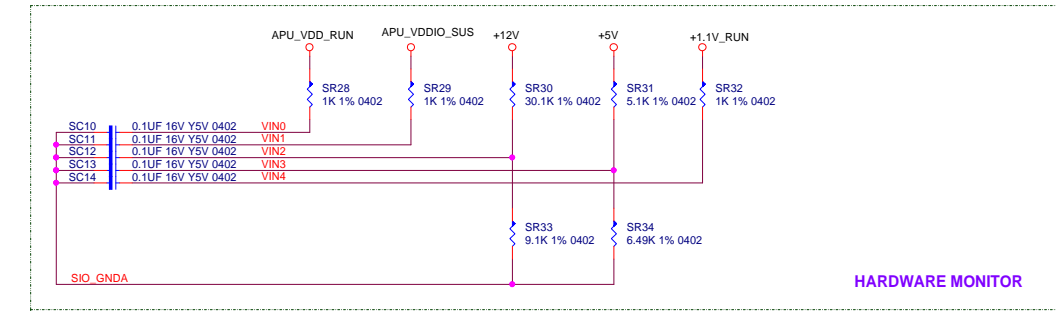
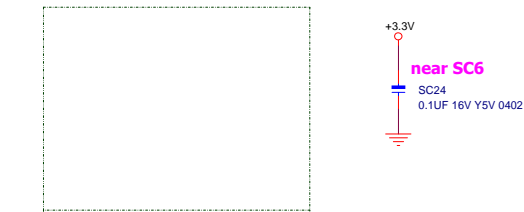
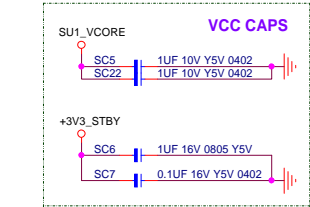
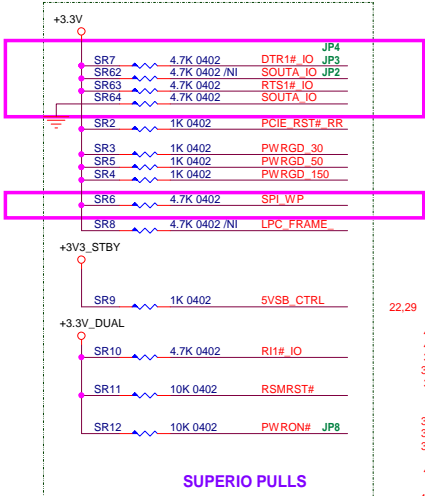


EMI solution

REAR AUDIO JACKS

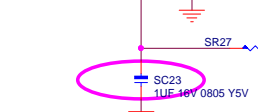
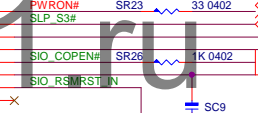
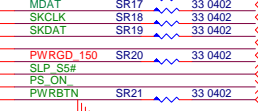
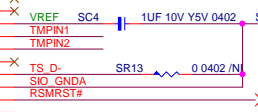
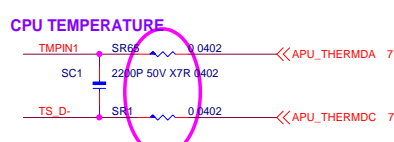
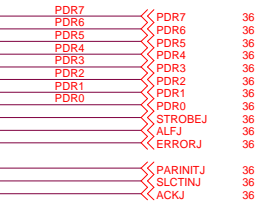


SUPERIO PART: S+Reference



IT8728FBX

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Power On Strapping Options

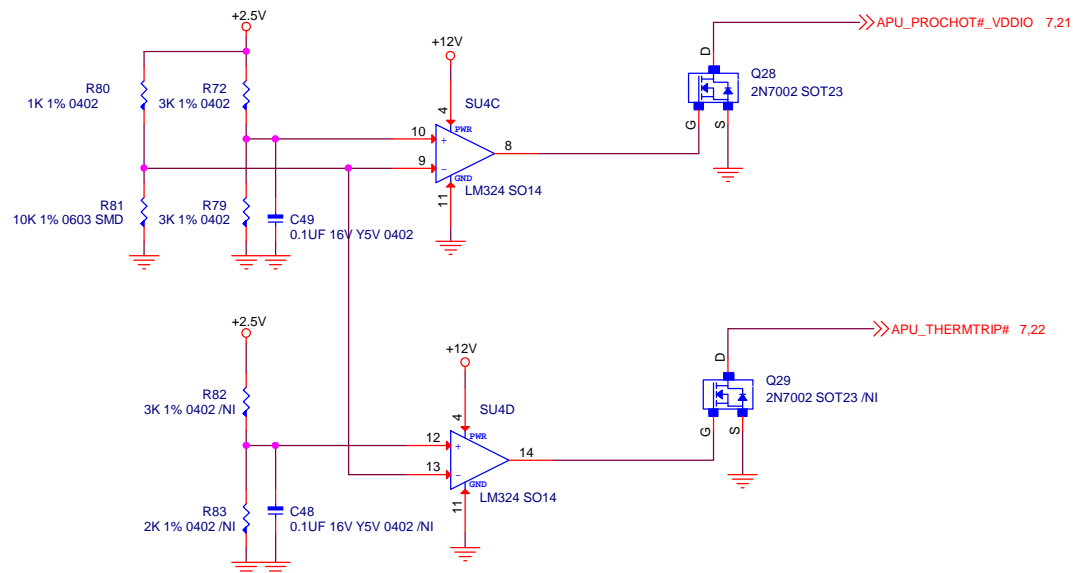
Symbol	value	Description
JP3	1	Disabled.
Pin 124	Flashseg1_EN	0 Flash I/F Address Segment 1 is enabled
JP4	K8PWR_EN	1 K8 power sequence function is disabled
Pin 126		0 K8 power sequence function is enabled
JP3 & JP5	FAN_CTL_SEL	11 The default value of EC Index 15h/16h/17h is 80h
Pin 124 & 46		10 The default value of EC Index 15h/16h/17h is FFh(Fan off)
		01 The default value of EC Index 15h/16h/17h is 00h(Fan full speed)
		00 The default value of EC Index 15h/16h/17h is 40h

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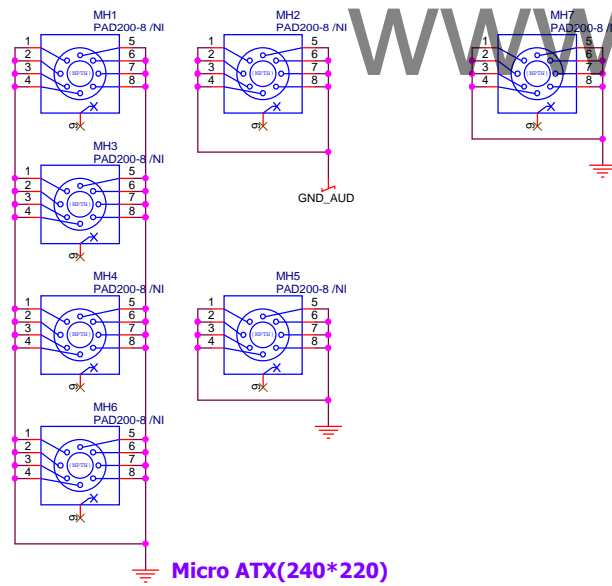


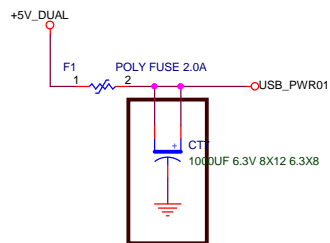
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Size	Document Number	AA55E-A1S		Rev	6.0
Custm					
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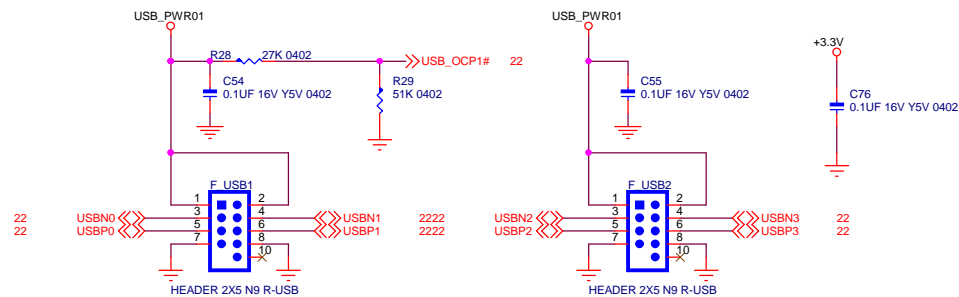
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6262 OVER VOLTAGE IC

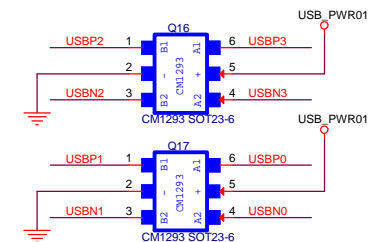




FRONT USB POWER

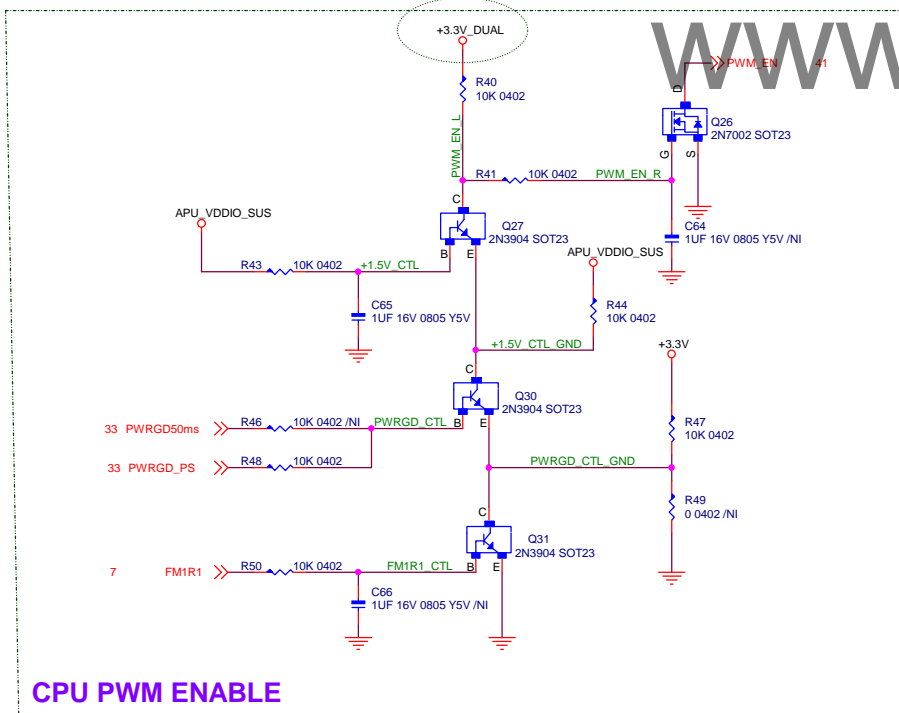
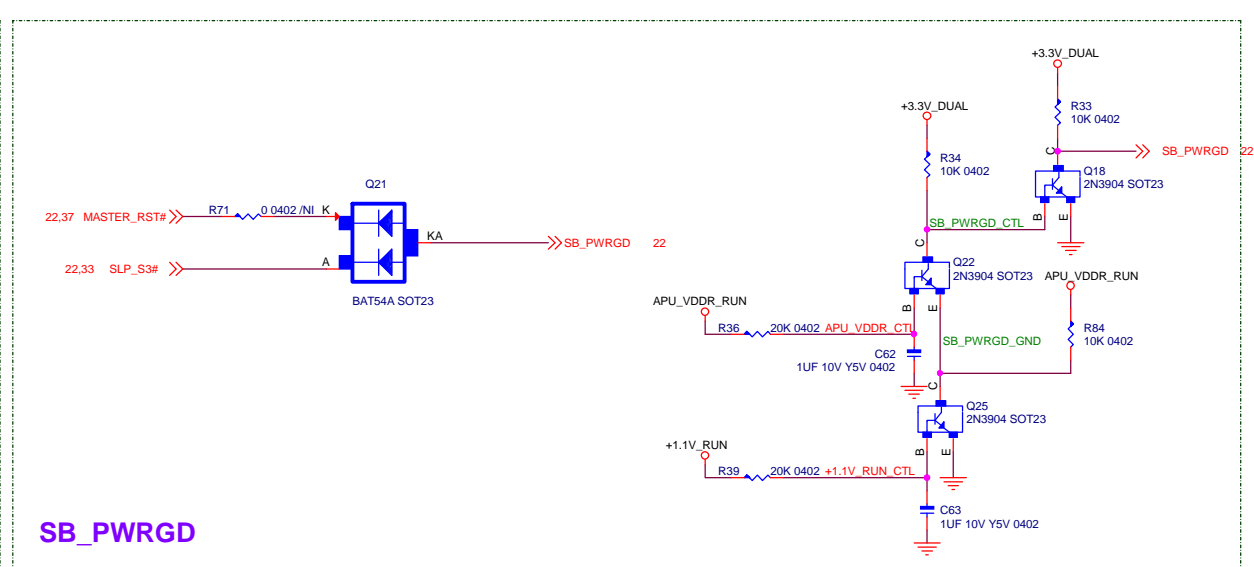
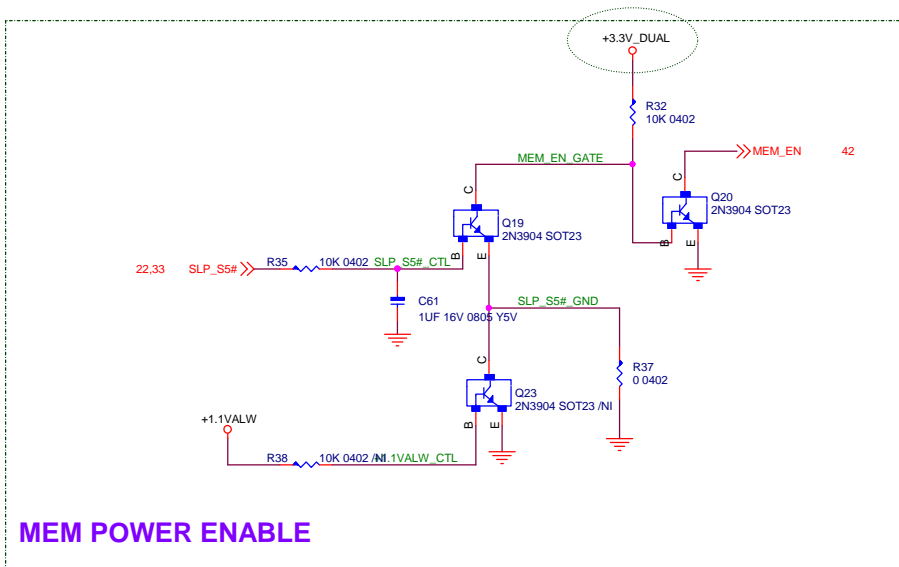


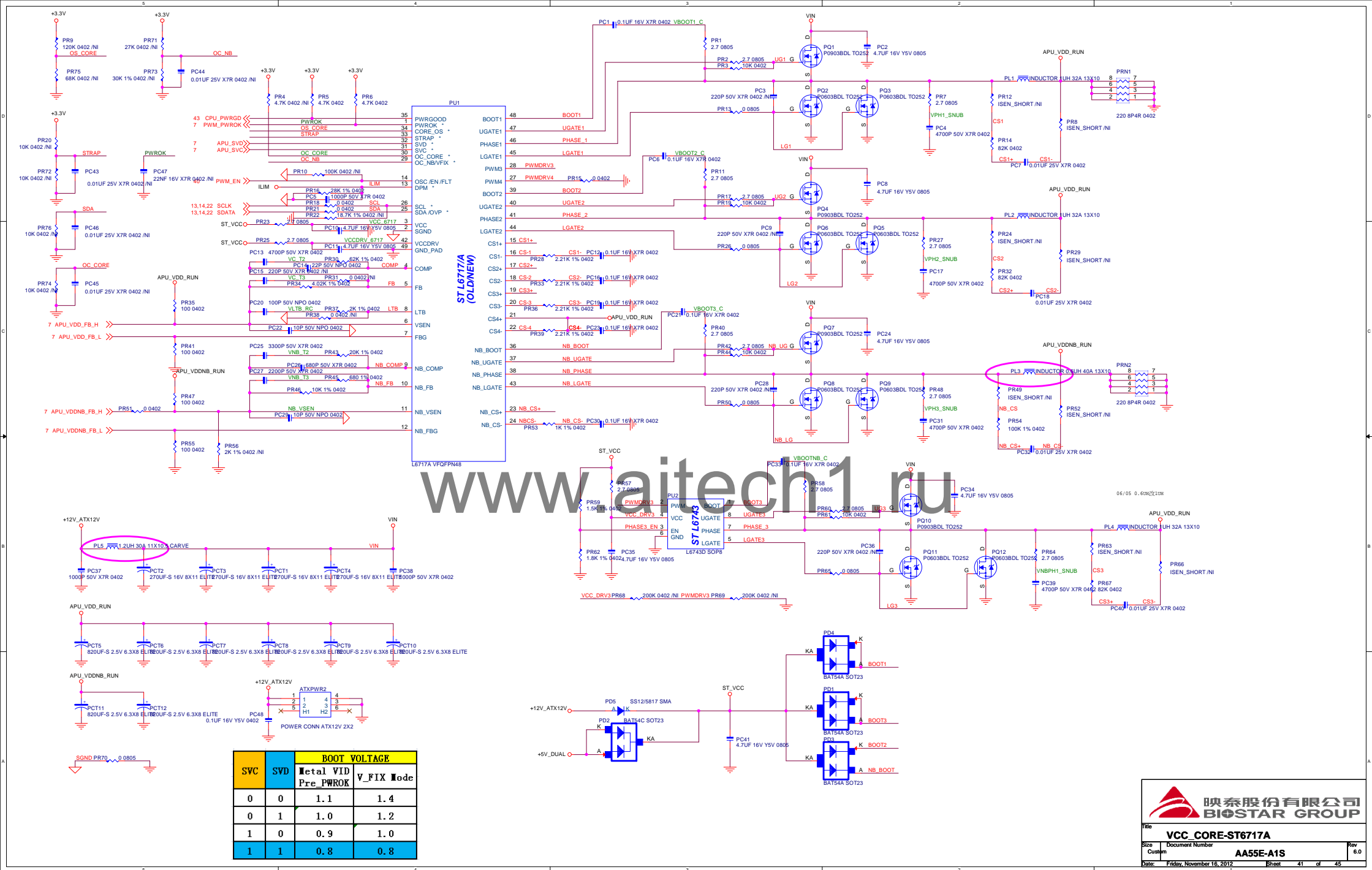
FRONT USB HEADER

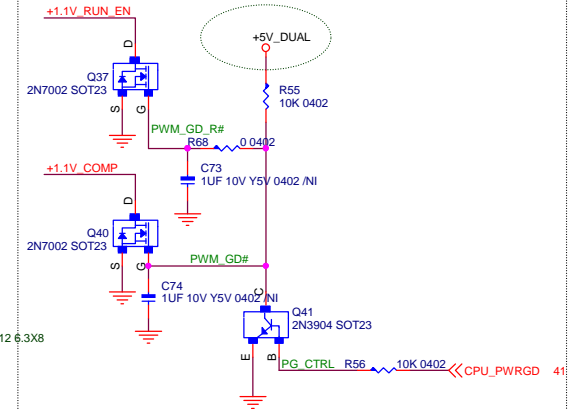
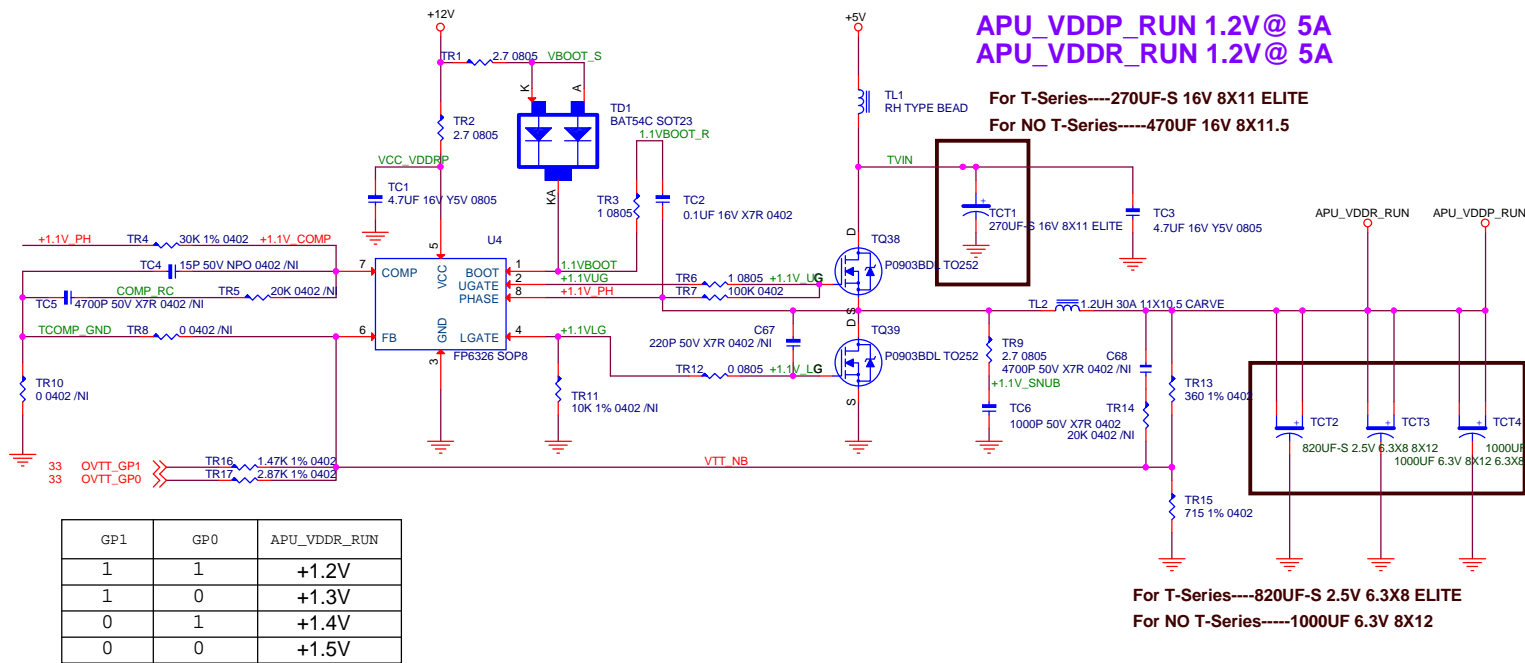


FRONT USB ESD

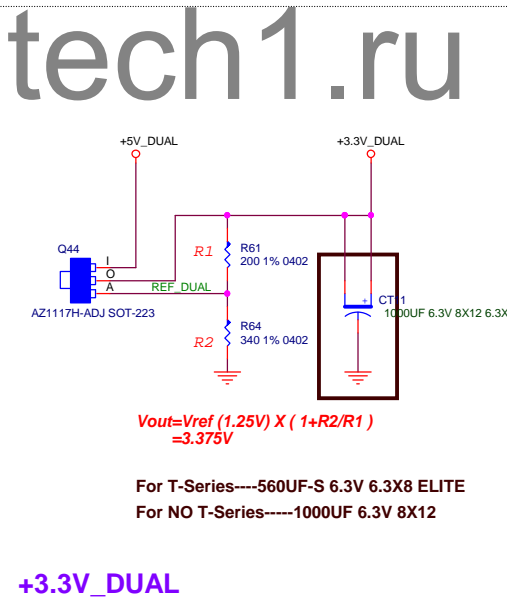
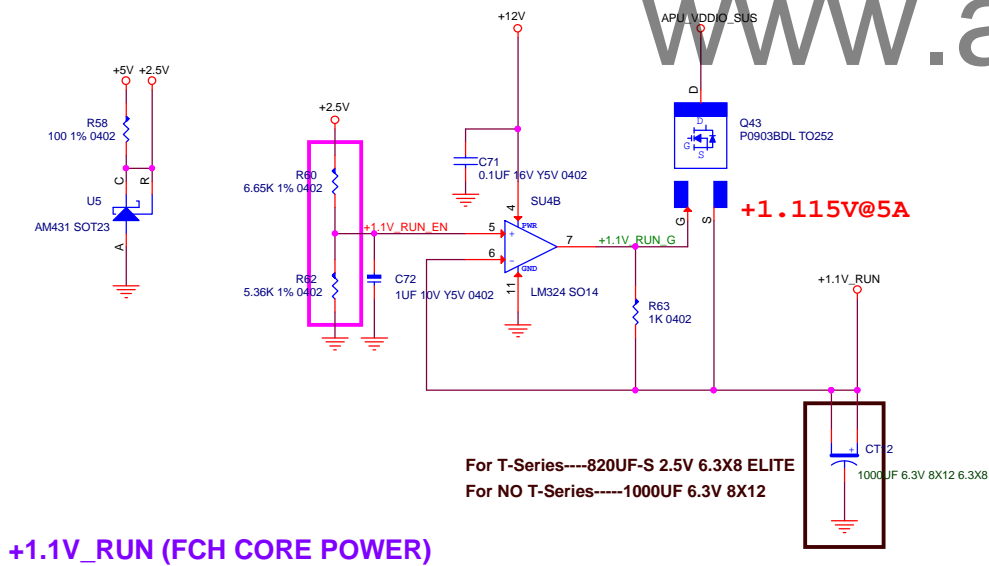
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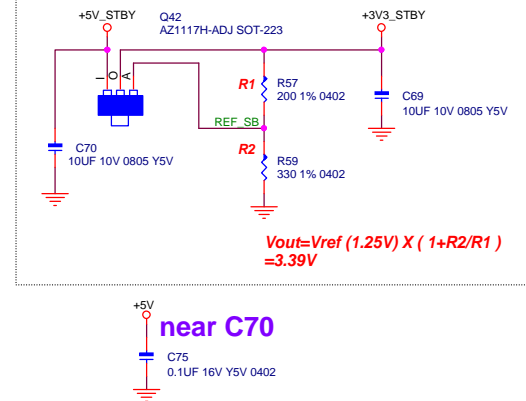


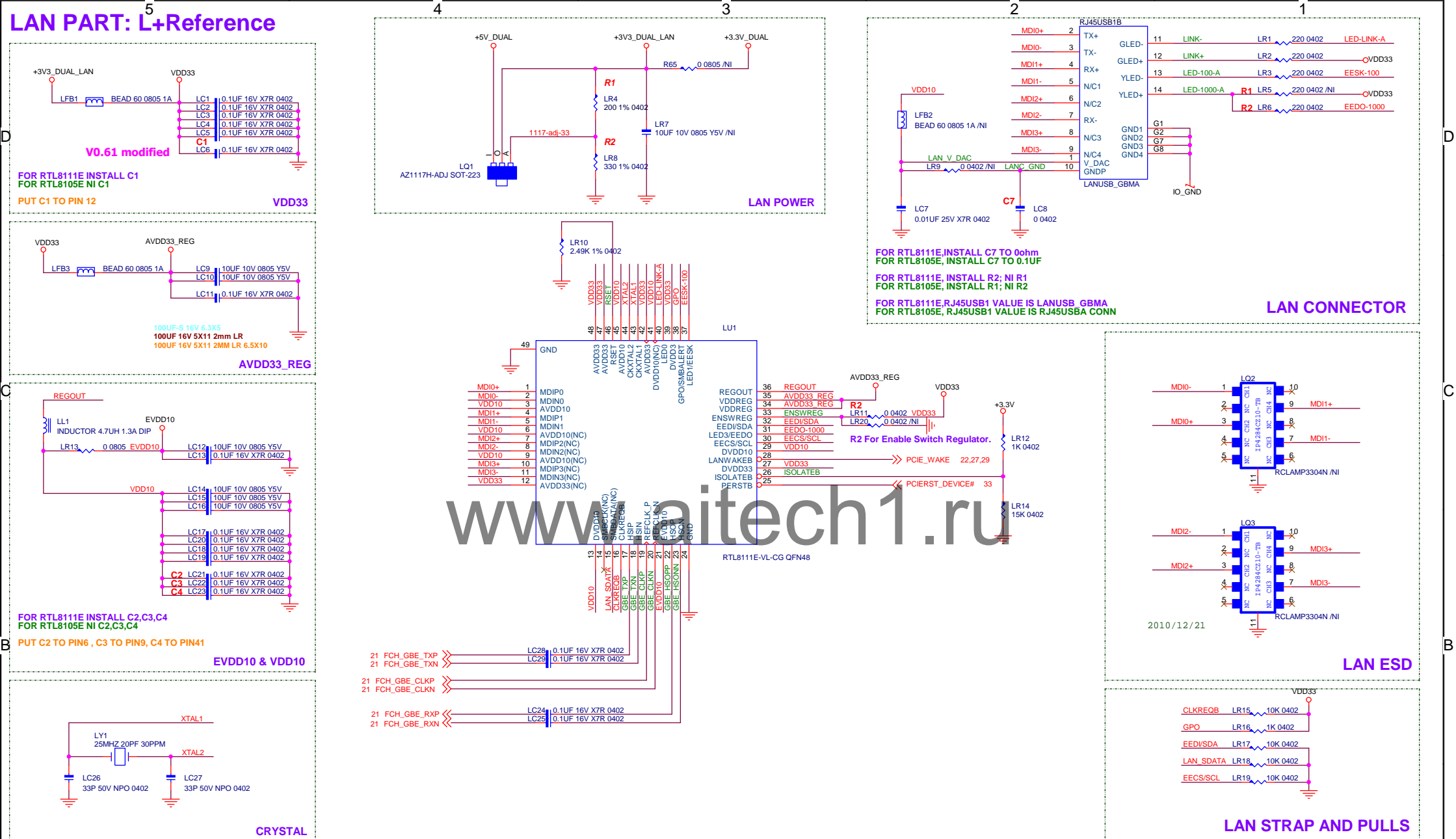


Power sequency



Energy-Using Product(EUP)





COMPONENT CONTRAST LIST

LAN PARTS	C1	C2	C3	C4	C7	R1	R2	R3	R4	RJ45 CONN
RTL8111E	O	O	O	O	0ohm	X	X	O	O	LANUSB_GBMA
RTL8105E	X	X	X	X	0.1uF	O	O	X	X	RJ45USBA CONN

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JCMOS1(1_2)
JUMPER 2P B



(YY1)
X'TAL WIRE

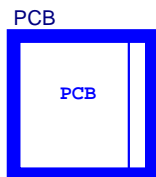


(BAT1)
電池
3V BATTERY SONY

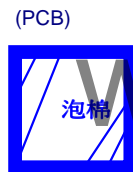
(ROM1)



FLASH ROM
W25Q16BVDA SO8



PCB
AA55E-A1S VER:6.0



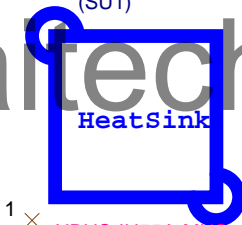
(PCB)
泡棉
POLON 245x200 /NI

(CPU1)



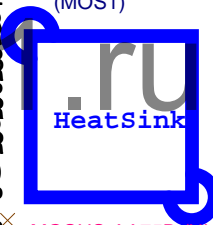
AM2RM-B

(SU1)



Heatsink
1 × NBHS-IH55A-MHS

(MOS1)



Heatsink
MOSHS-AA75B /NI

Notes:

- 1.(SU1)----->HS_SU1,while netin,
- 2.HS_SU1----->(SU1),while Bom out.

Notes:

- 1.HS_MOS1----->while netin
- 2.(MOS1)----->while Bom out

20-120-404000H11 汎
NBHS-IH55A-MHS

42x42x13mm/?伎/遷證/

糯H1-NB150-HS07/

. 扭

20-120-105300H1

MOS汎

/?伎/105.3x30.7x22.3mm/(0288-0175F)/(HF)/.KG MOSHS-AA75B



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Title

BOM

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